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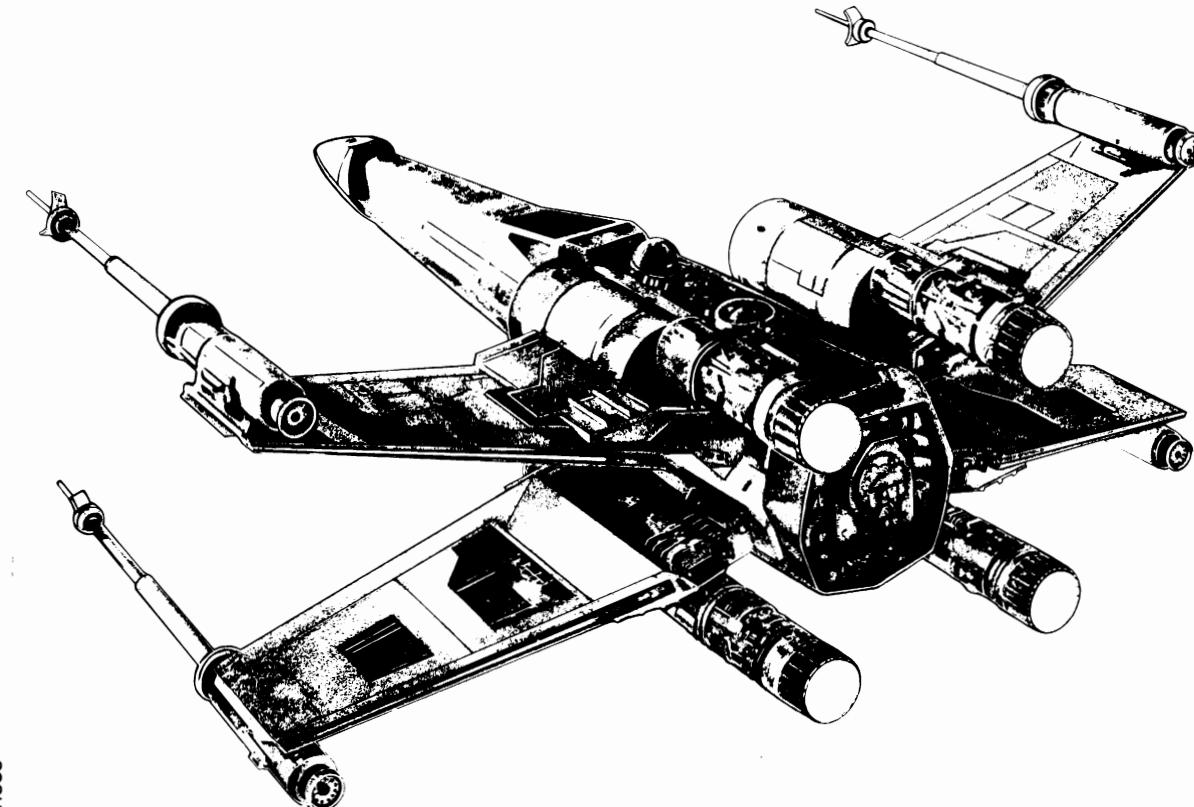
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NOTE

A STAR WARS Troubleshooting Guide is included as part of this Schematic Package Supplement. The STAR WARS Troubleshooting Guide contains Diagnostic Hardware tests and Atari CAT Box troubleshooting procedures.



NOTE
This staple temporarily holds the schematic package together. Remove the staple before using these schematics.

Schematic Package Supplement to

**STAR^{*}
WARS**

Operators Manual

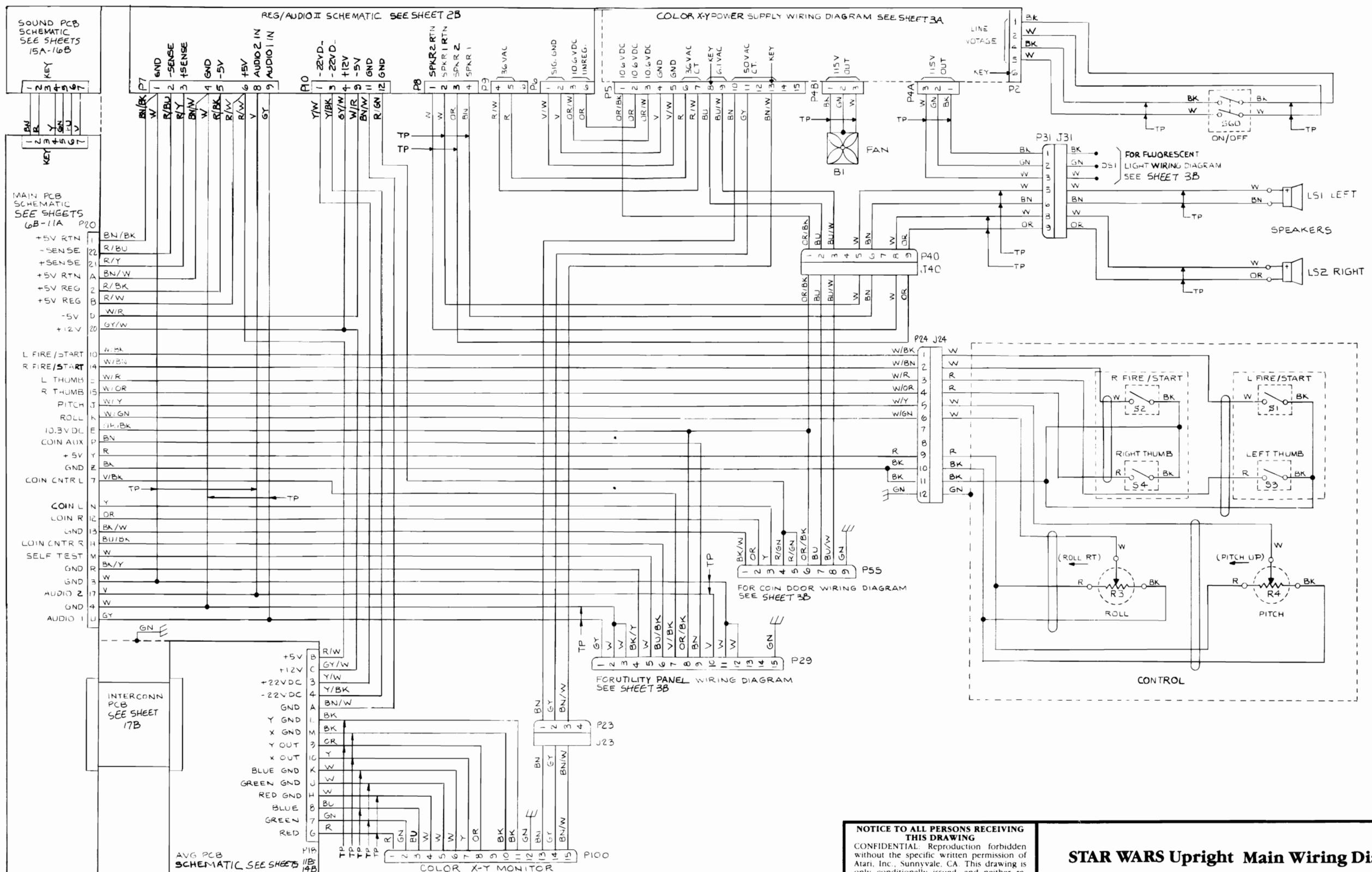
Includes Troubleshooting Guide

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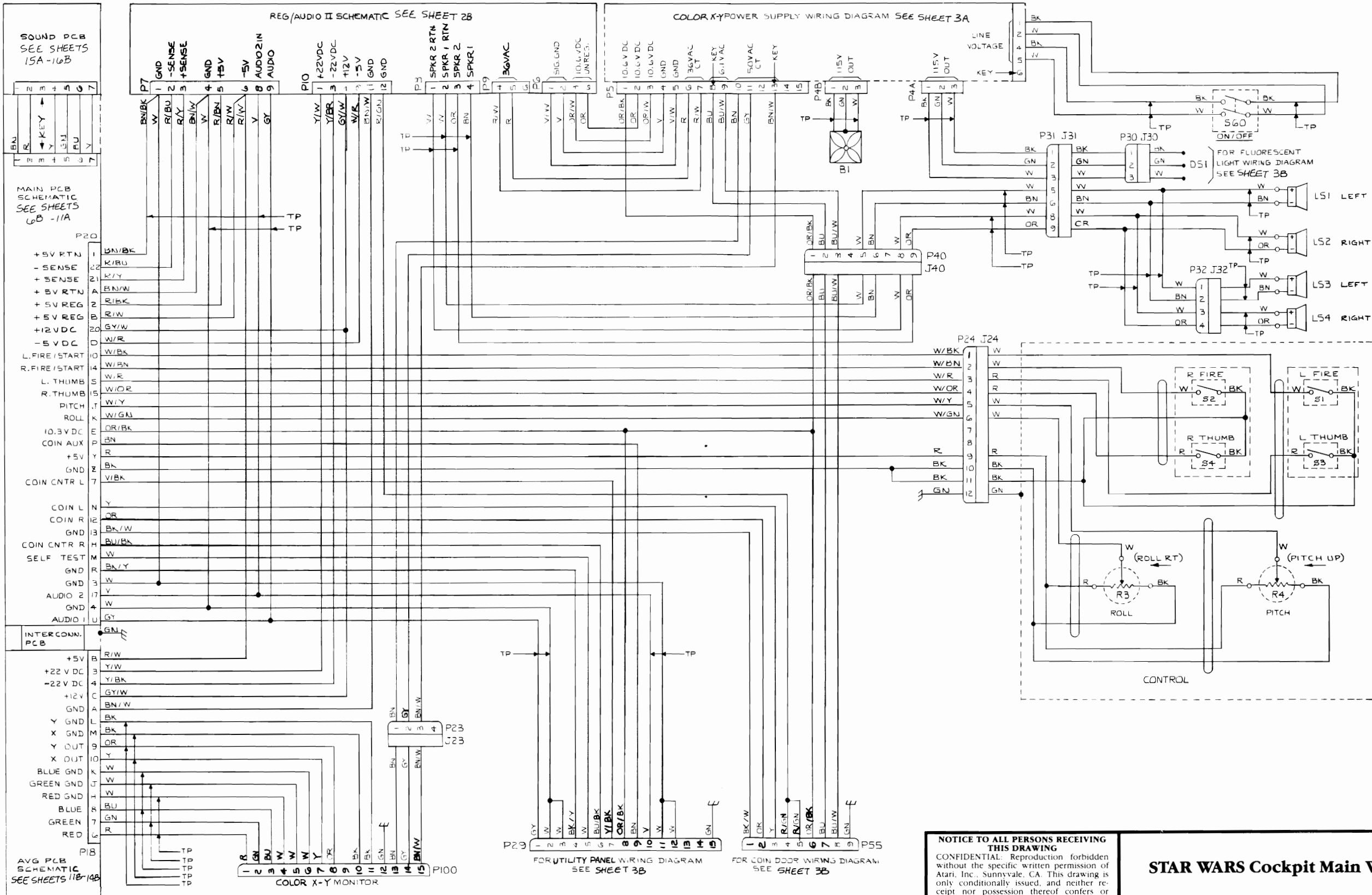
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STAR WARS Upright Main Wiring Diagram



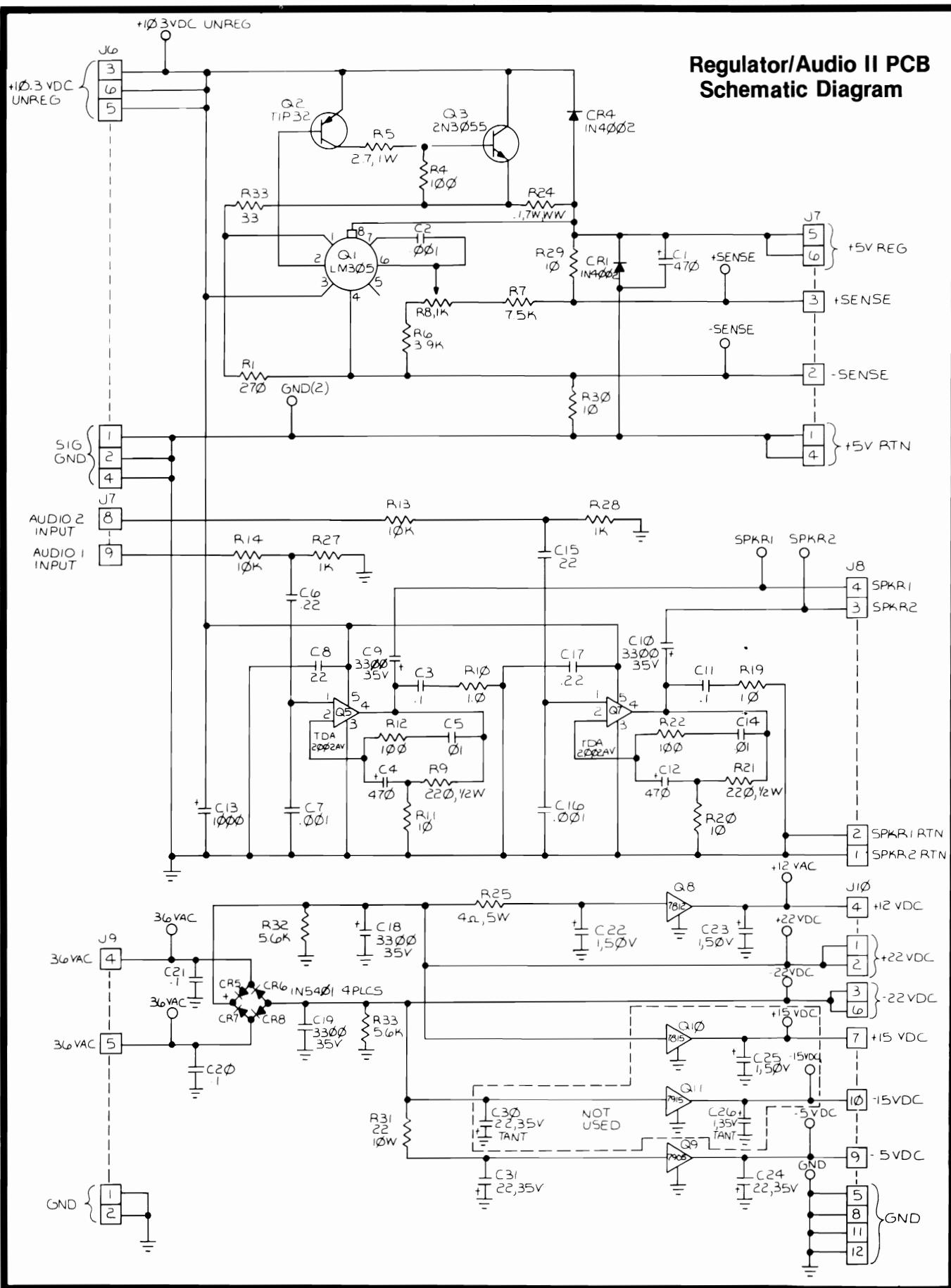
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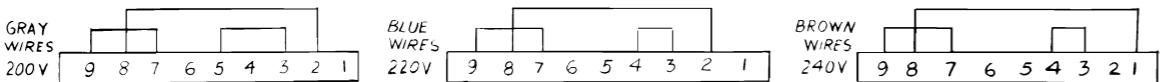


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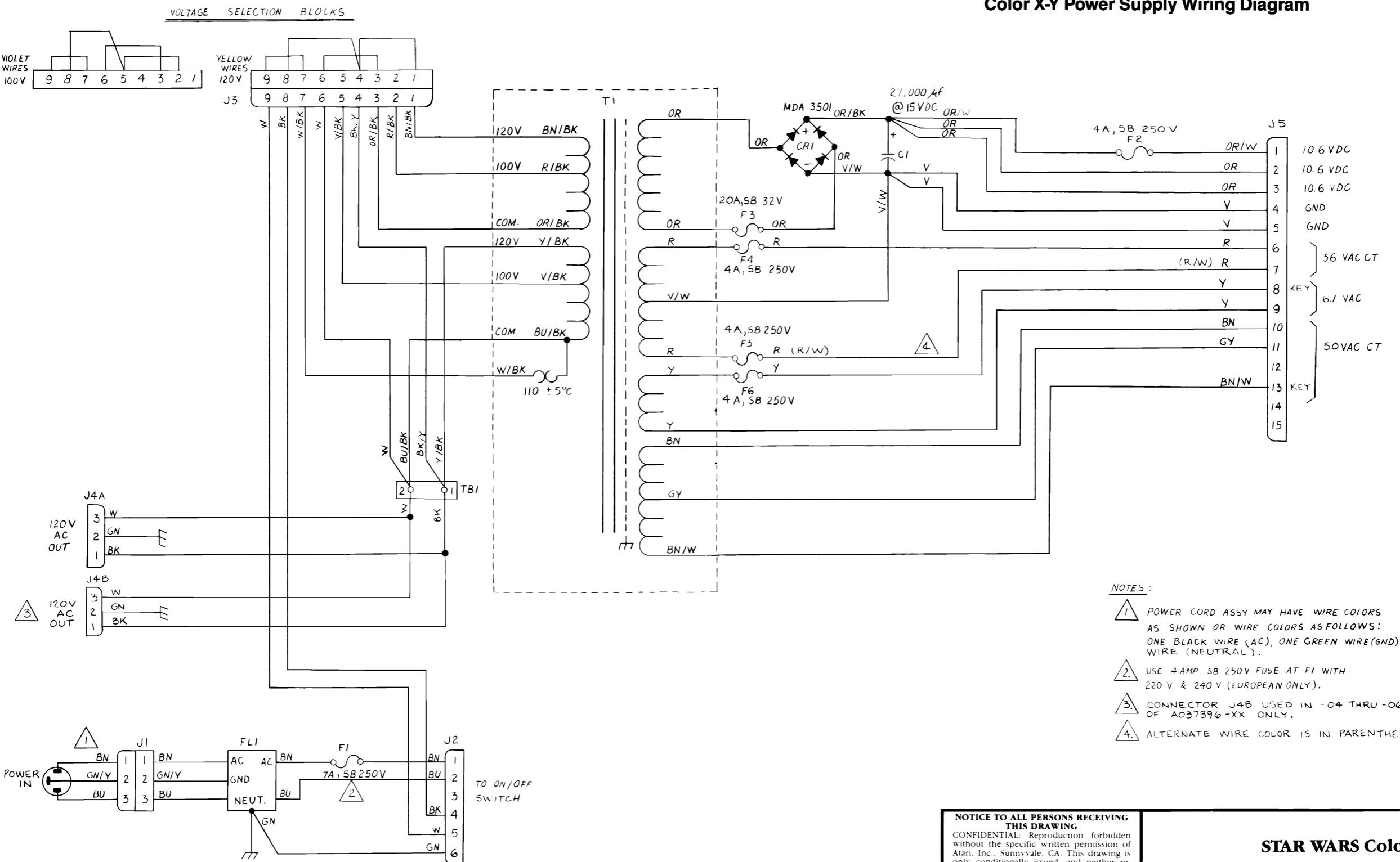
STAR WARS Regulator/Audio II PCB



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Color X-Y Power Supply Wiring Diagram



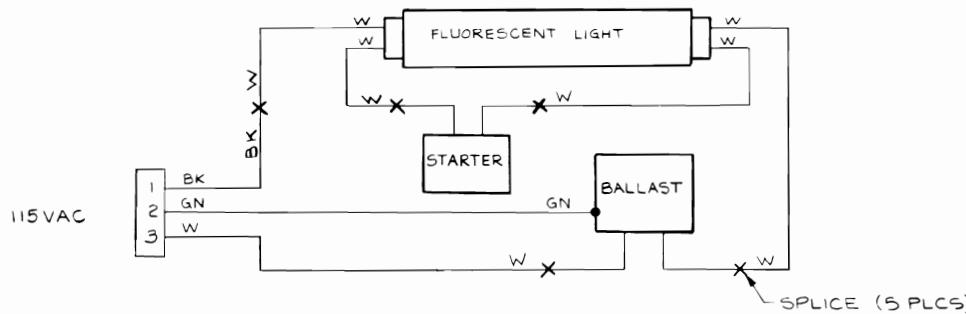
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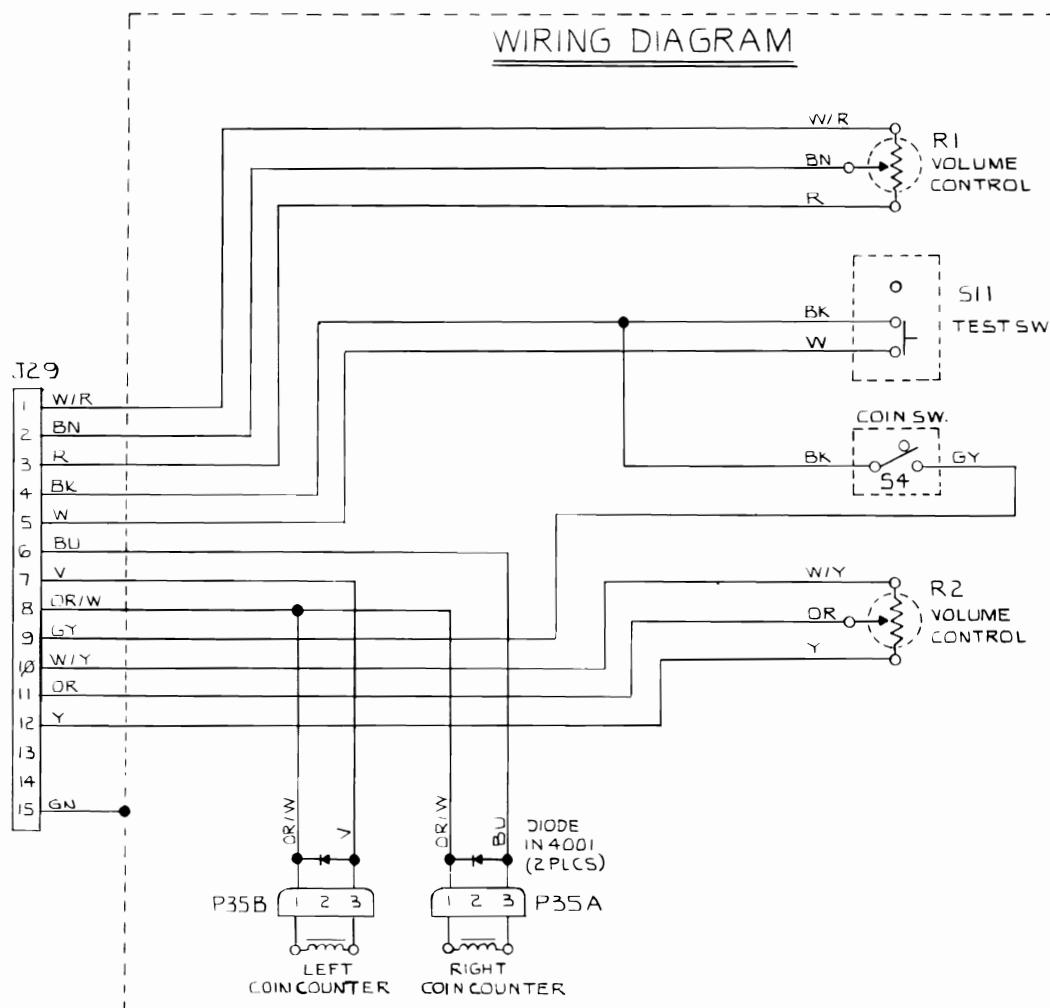
STAR WARS Color X-Y Power Supply Wiring Diagram



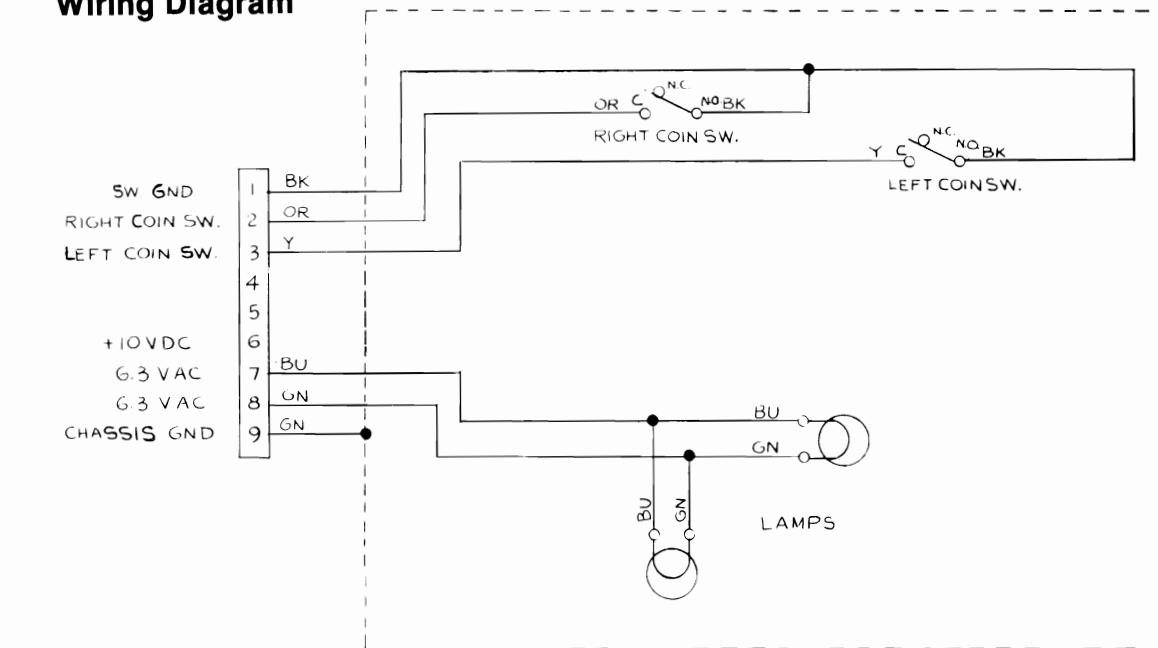
Fluorescent Light Wiring Diagram



Utility Panel Wiring Diagram



Coin Door Wiring Diagram



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STAR WARS Game Interfaces



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Main PCB

HEXA-DECIMAL ADDRESS	ADDRESS BUS SIGNAL LINES												READ/WRITE	DATA BUS SIGNAL LINES								FUNCTION			
	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	D7	D6	D5	D4	D3	D2	D1	D0	
0000-07FF	0	0	0	0	0	0	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM0
0800-0FFF	0	0	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM1
1000-17FF	0	0	0	1	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM2
1800-1FFF	0	0	0	1	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM3
2000-27FF	0	0	1	0	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM4
2800-2FFF	0	0	1	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VARAM5
3000-3FFF	0	0	1	1	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	VROM
4300	0	1	0	0	0	0	0	1	1	0	0	0				R	D								LEFT F/S
	0	1	0	0	0	0	1	1	0	0	0					R	D								RIGHT F/S
	0	1	0	0	0	0	0	1	1	0	0					R	D								SPARE 1
	0	1	0	0	0	0	0	1	1	0	0					R	D								SELF-TEST
	0	1	0	0	0	0	0	1	1	0	0					R	D								SLAM
	0	1	0	0	0	0	0	1	1	0	0					R	D								COIN AUX
	0	1	0	0	0	0	0	1	1	0	0					R	D								COIN L
	0	1	0	0	0	0	0	1	1	0	0					R	D								COIN R
	0	1	0	0	0	0	0	1	1	0	0														NSTORE
4320	0	1	0	0	0	0	0	1	1	0	0	1				R	D								MATH RUN
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								VGHALT
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								LEFT THUMB
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								RIGHT THUMB
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								SPARE 2
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								DIAGN
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								Unused
	0	1	0	0	0	0	0	1	1	0	0	1				R	D								Unused
	0	1	0	0	0	0	0	1	1	0	1	0				R	D								OPT0
4340	0	1	0	0	0	0	0	1	1	0	1	0				R	D	D	D	D	D	D	D	D	OPT1
4360	0	1	0	0	0	0	0	1	1	0	1	1				R	D	D	D	D	D	D	D	D	ADC
4380	0	1	0	0	0	0	0	1	1	1	0	0				R	D	D	D	D	D	D	D	D	MAINREAD
4400	0	1	0	0	0	0	1	0	0				0		R	D	D	D	D	D	D	D	D	MAINWR	
4401	0	1	0	0	0	0	1	0	0				0		R	D	D	D	D	D	D	D	D	MAIN READY FLAG	
4401	0	1	0	0	0	0	1	0	0				1		R	D	D	D	D	D	D	D	D	SOUND READY FLAG	
4500-45FF	0	1	0	0	0	0	1	0	1	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	NOVRAM

HEXA-DECIMAL ADDRESS	ADDRESS BUS SIGNAL LINES												READ/WRITE	DATA BUS SIGNAL LINES								FUNCTION			
	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	D7	D6	D5	D4	D3	D2	D1	D0	
4600	0	1	0	0	0	0	1	1	0	0	0	0				R/W									EVGGO
4620	0	1	0	0	0	1	1	0	0	0	1					R/W									EVGRES
4640	0	1	0	0	0	1	1	0	0	1	0					R/W									WDCLR
4660	0	1	0	0	0	1	1	0	0	1	1					R/W									IRQCLR
4680	0	1	0	0	0	1	1	0	1	0	0					W									COIN CNTR 2
4681	0	1	0	0	0	1	1	0	1	0	0					W									COIN CNTR 1
4682	0	1	0	0	0	1	1	0	1	0	0					W									LED 3
4683	0	1	0	0	0	1	1	0	1	0	0					W									LED 2
4684	0	1	0	0	0	1	1	0	1	0	0					W									MPAGE
4685	0	1	0	0	0	1	1	0	1	0	0														

Sound PCB

HEXA-DECIMAL ADDRESS	ADDRESS BUS SIGNAL LINES A15 A14 A13 A12 A11 A10 A9 A8 A7 A6 A5 A4 A3 A2 A1 A0	READ/ WRITE	DATA BUS SIGNAL LINES								FUNCTION
			D7	D6	D5	D4	D3	D2	D1	D0	
0000	0 0 0 0	W	D	D	D	D	D	D	D	D	SOUT
0800	0 0 0 1	R	D	D	D	D	D	D	D	D	SIN
1000-107F	0 0 1 0 0 A A A A A A	R/W	D	D	D	D	D	D	D	D	PIA (RAM)
1080	0 0 1 0 1 0 0 0 0	R/W	D	D	D	D	D	D	D	D	PIA (PORT A DATA)
1081	0 0 1 0 1 0 0 0 1	R/W	D	D	D	D	D	D	D	D	PIA (PORT A DIR)
1082	0 0 1 0 1 0 0 1 0	R/W	D	D	D	D	D	D	D	D	PIA (PORT B DATA)
1083	0 0 1 0 1 0 0 1 1	R/W	D	D	D	D	D	D	D	D	PIA (PORT B DIR)
1084-109F	0 0 1 0 1 1 A 1 A A	R/W	D	D	D	D	D	D	D	D	PIA (CONTROL)
1800-1807	0 0 1 1 0 0 0 A A A	R/W	D	D	D	D	D	D	D	D	CIO-0 (SOUND)
1808-180F	0 0 1 1 0 0 1 A A A	R/W	D	D	D	D	D	D	D	D	CIO-1 (SOUND)
1810-1817	0 0 1 1 0 1 0 A A A	R/W	D	D	D	D	D	D	D	D	CIO-2 (SOUND)
1818-181F	0 0 1 1 0 1 1 A A A	R/W	D	D	D	D	D	D	D	D	CIO-3 (SOUND)
1820-1827	0 0 1 1 1 0 0 A A A	R/W	D	D	D	D	D	D	D	D	CIO-0 (CONTROL)
1828-182F	0 0 1 1 1 0 1 A A A	R/W	D	D	D	D	D	D	D	D	CIO-1 (CONTROL)
1830-1837	0 0 1 1 1 1 0 A A A	R/W	D	D	D	D	D	D	D	D	CIO-2 (CONTROL)
1838-183F	0 0 1 1 1 1 1 A A A	R/W	D	D	D	D	D	D	D	D	CIO-3 (CONTROL)
2000-27FF	0 1 A A A A A A A A A A	R/W	D	D	D	D	D	D	D	D	PROGRAM RAM
4000-5FFF	1 0 A A A A A A A A A A	R	D	D	D	D	D	D	D	D	SROM0
6000-7FFF	1 1 A A A A A A A A A A	R	D	D	D	D	D	D	D	D	SROM1

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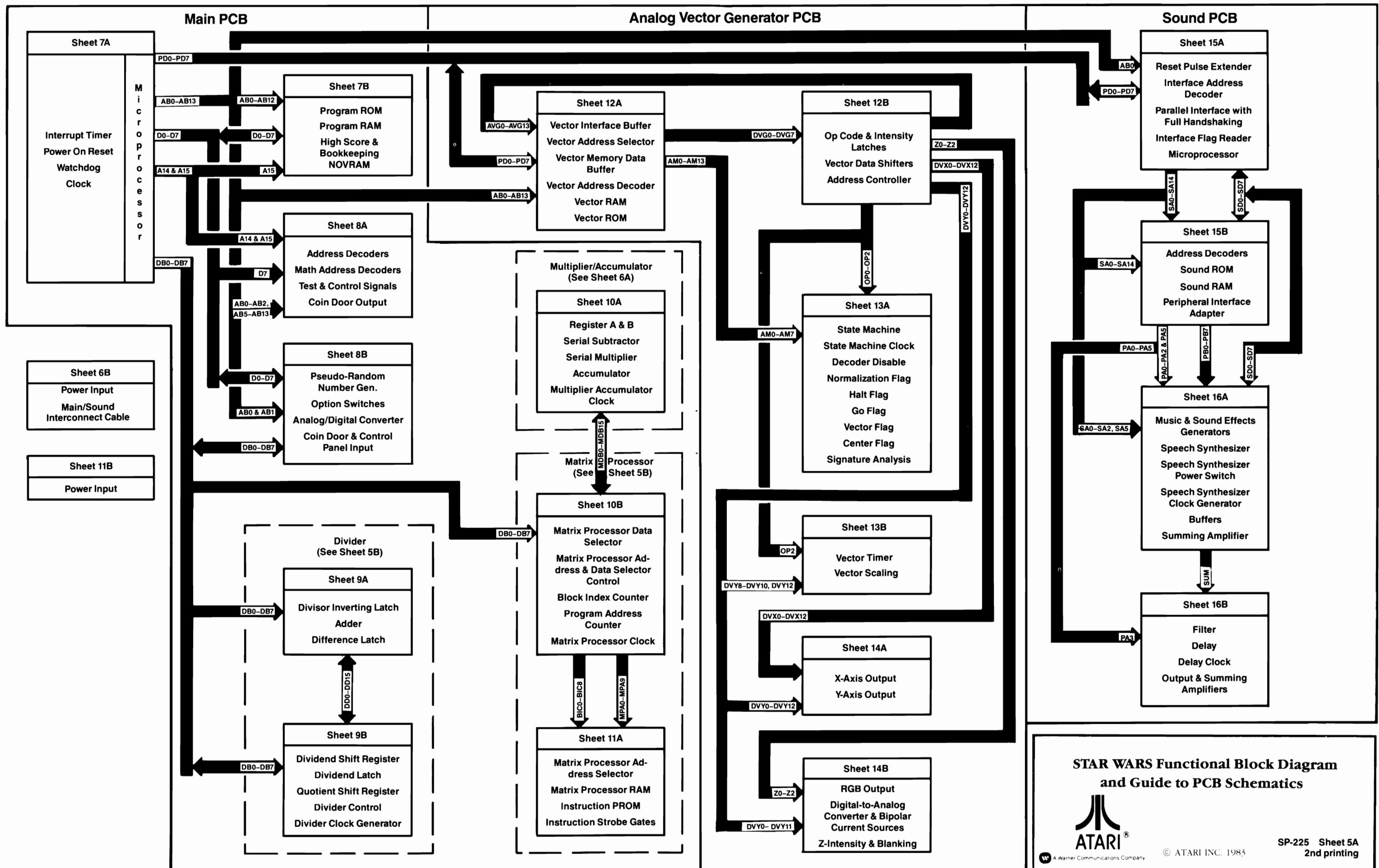
STAR WARS Memory Map -part 2

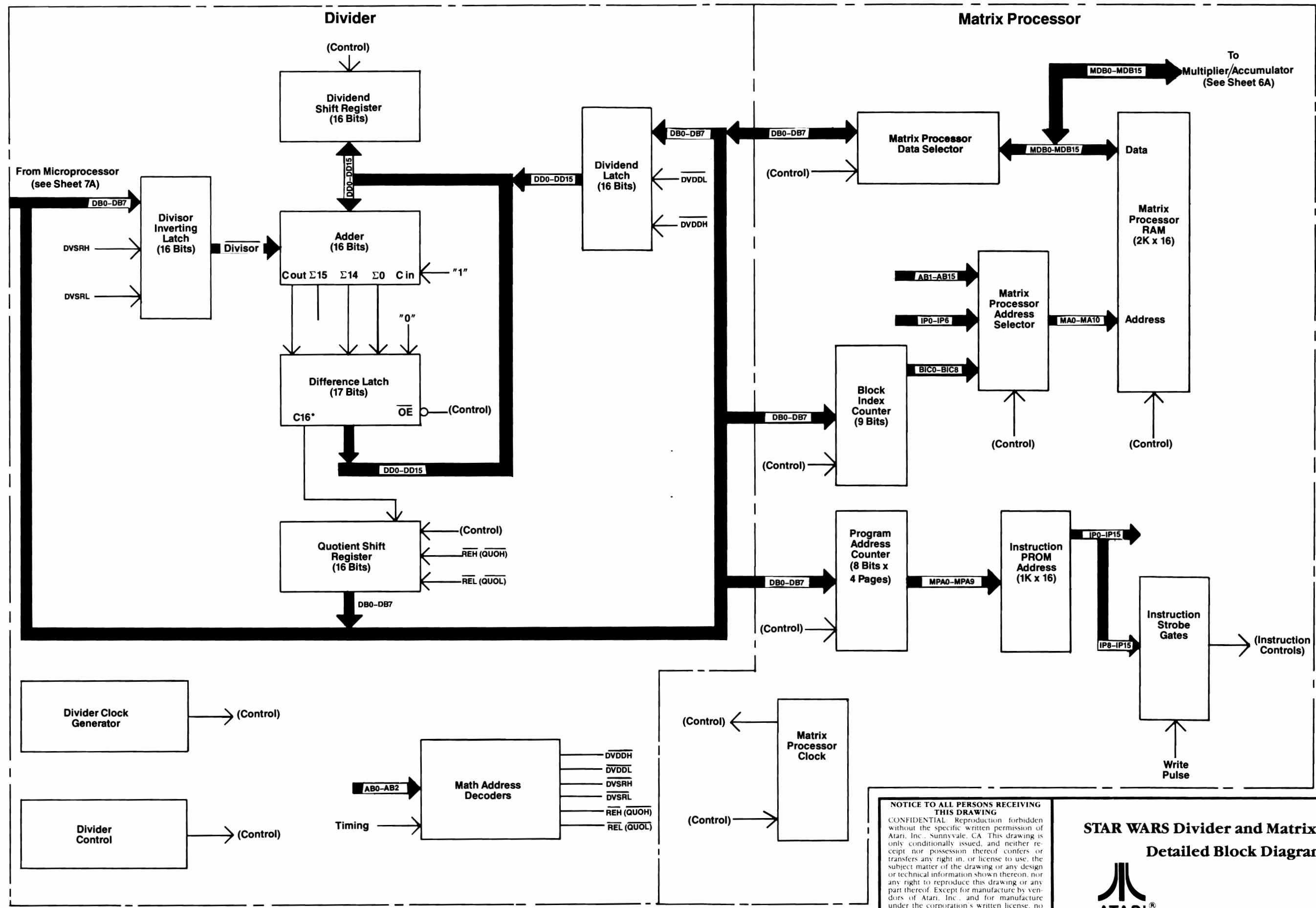


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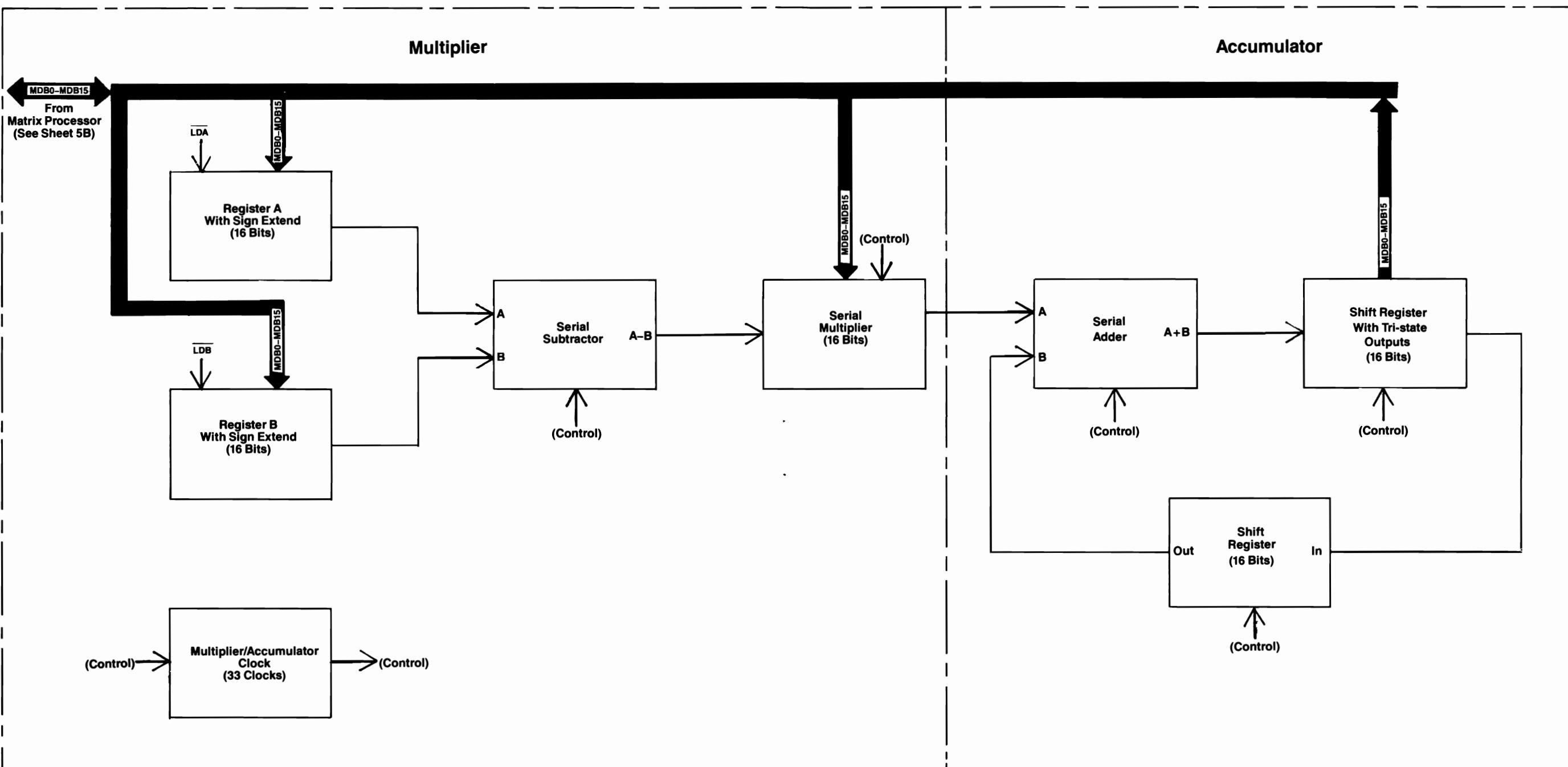
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STAR WARS Divider and Matrix Processor Detailed Block Diagrams



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Multiplier/Accumulator
 $ACC = ACC + (A-B) \times C$

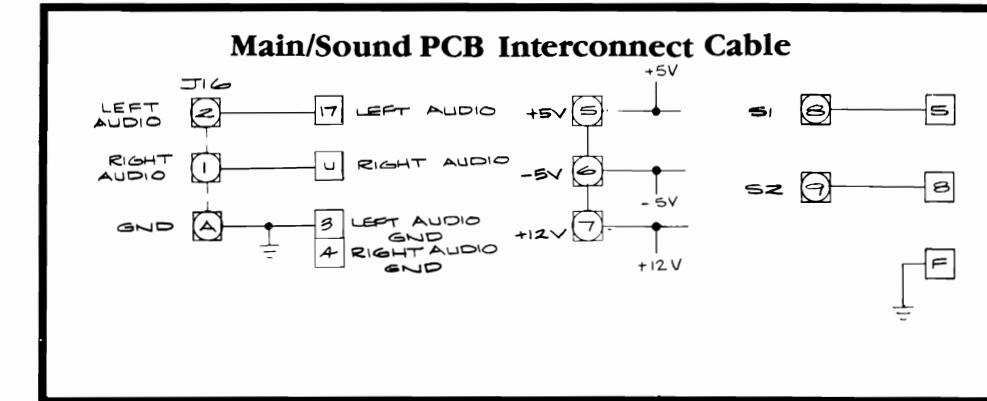
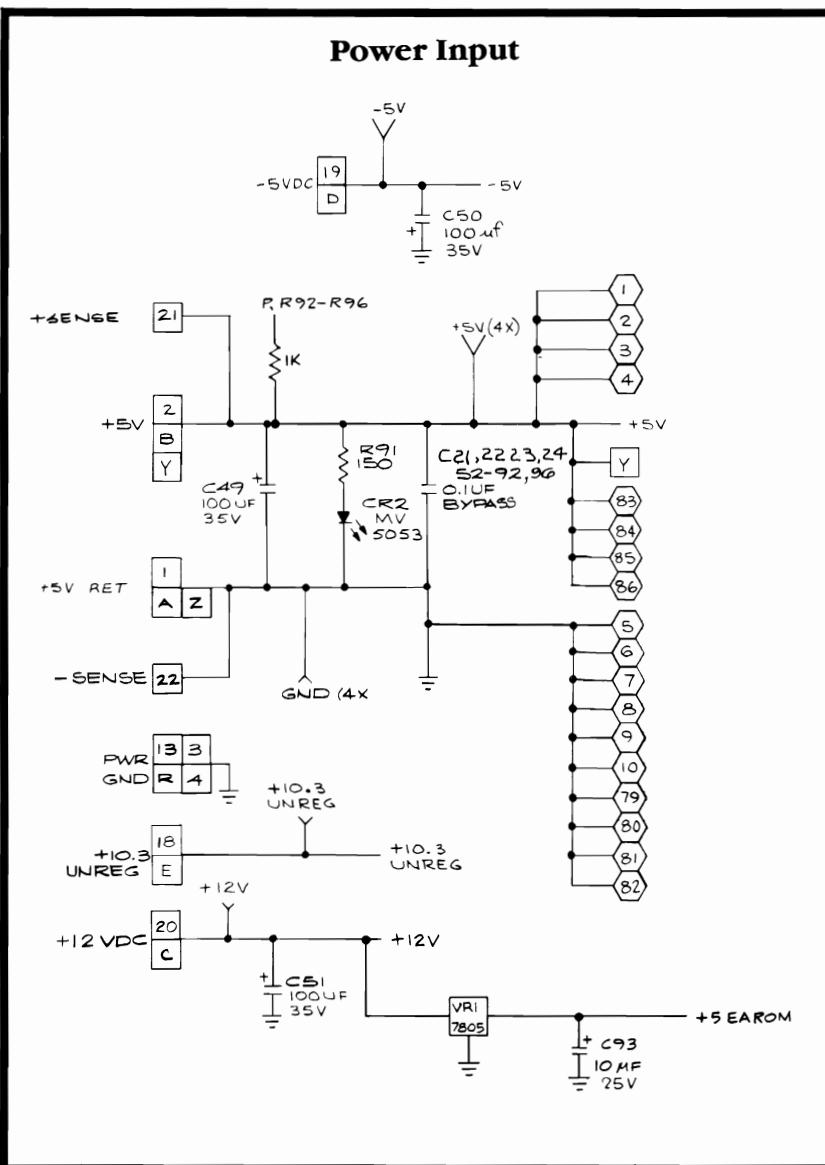


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STAR WARS Multiplier/Accumulator
Detailed Block Diagram



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NOTE

- = J20 CONNECTOR
- = TEST CONNECTOR
- ◇ = J17 CONNECTOR
- ◎ = J16 CONNECTOR

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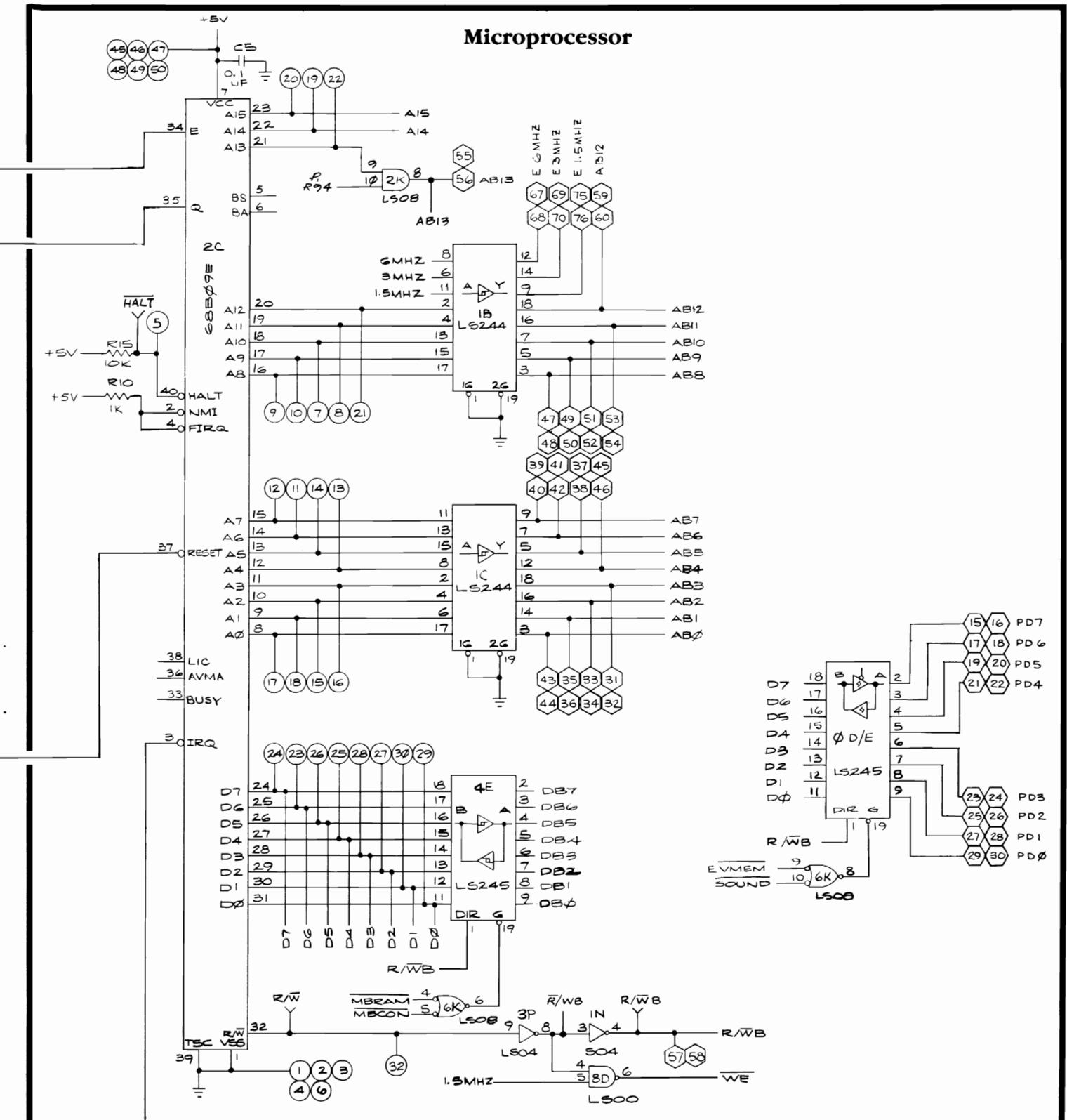
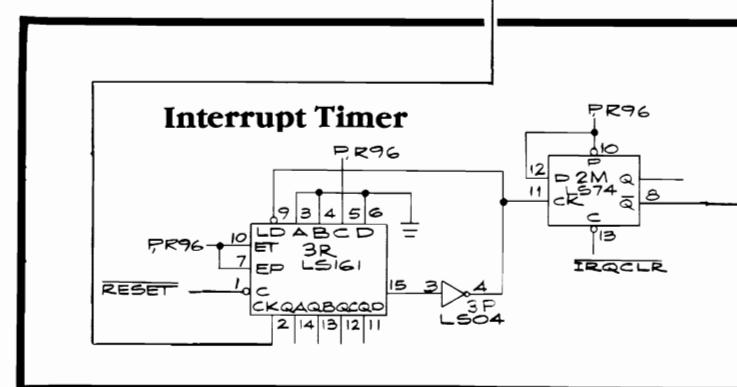
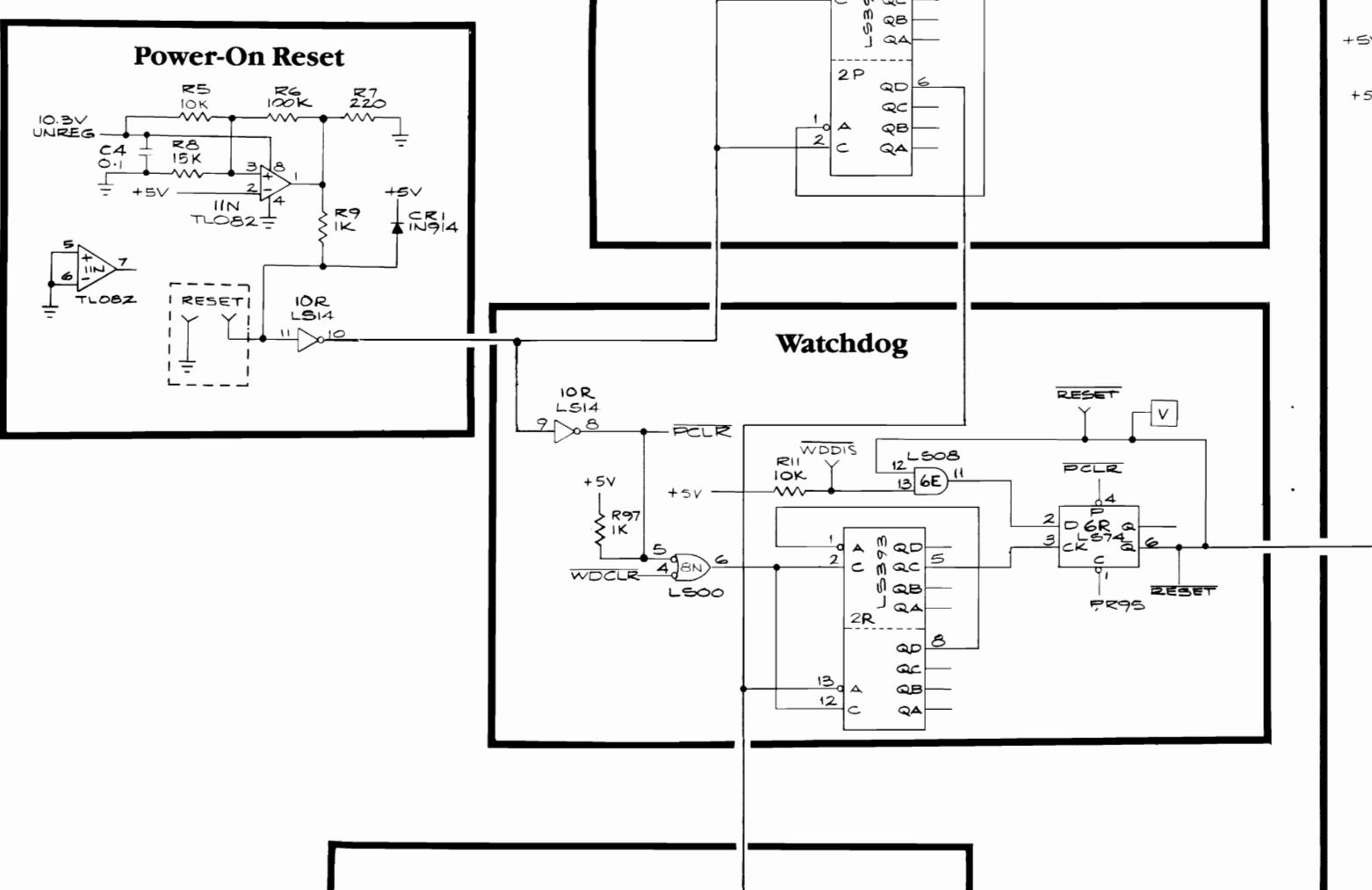
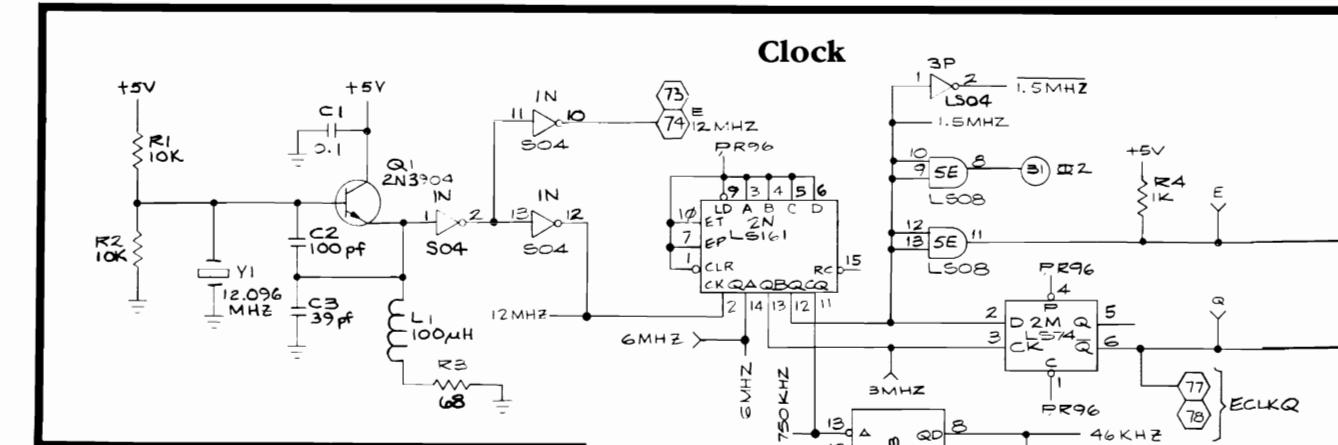
STAR WARS Main PCB



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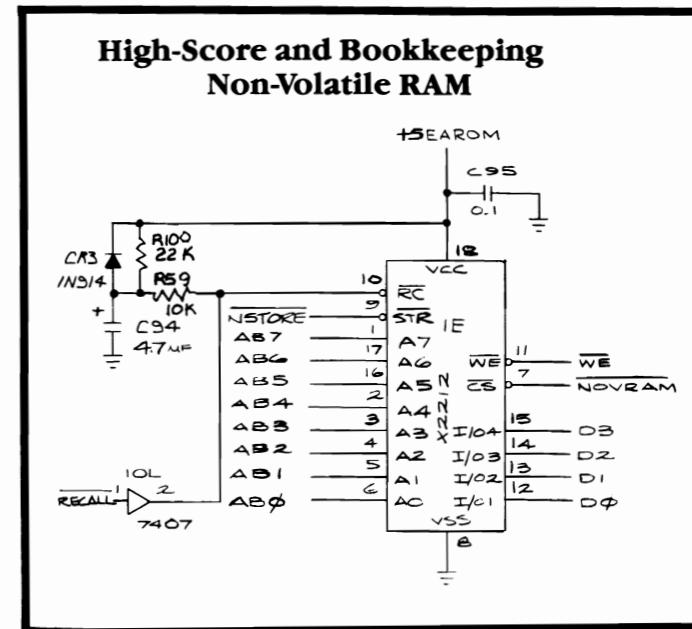
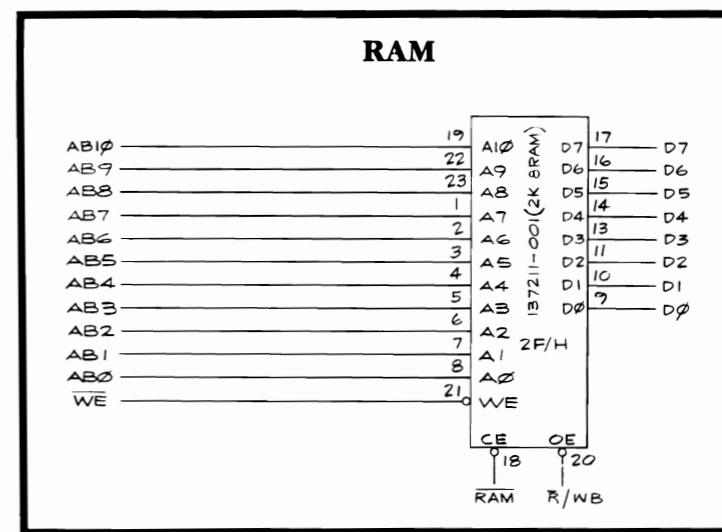
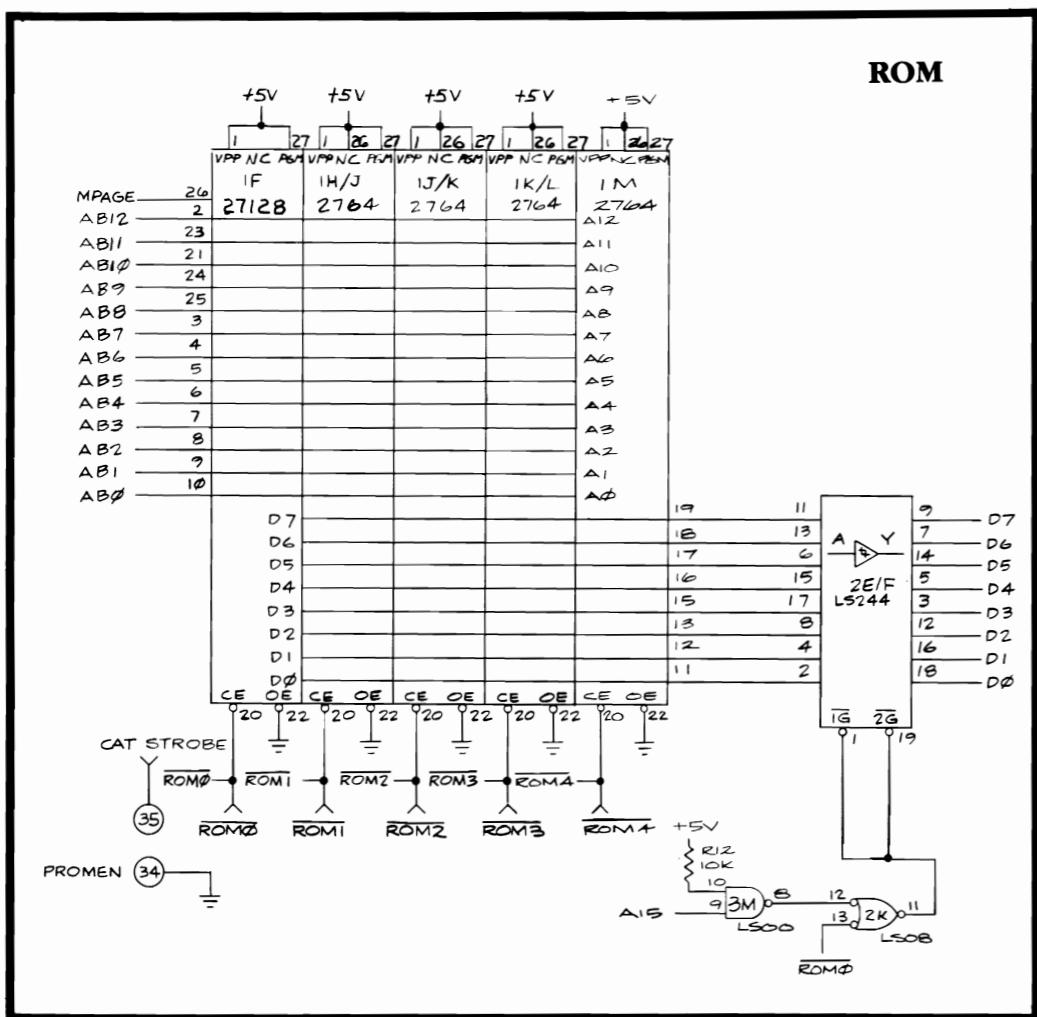
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STAR WARS Main PCB



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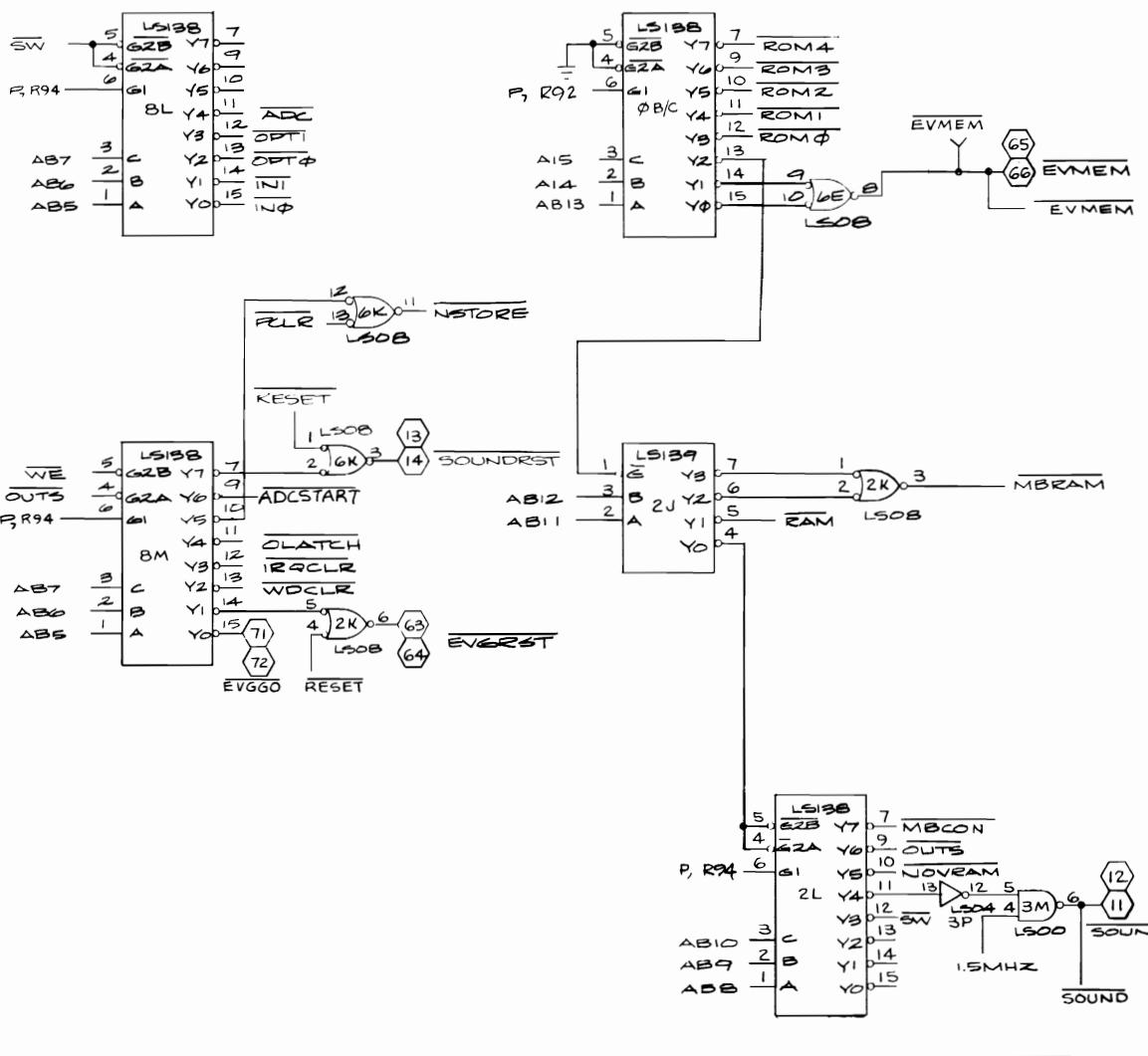
STAR WARS Main PCB



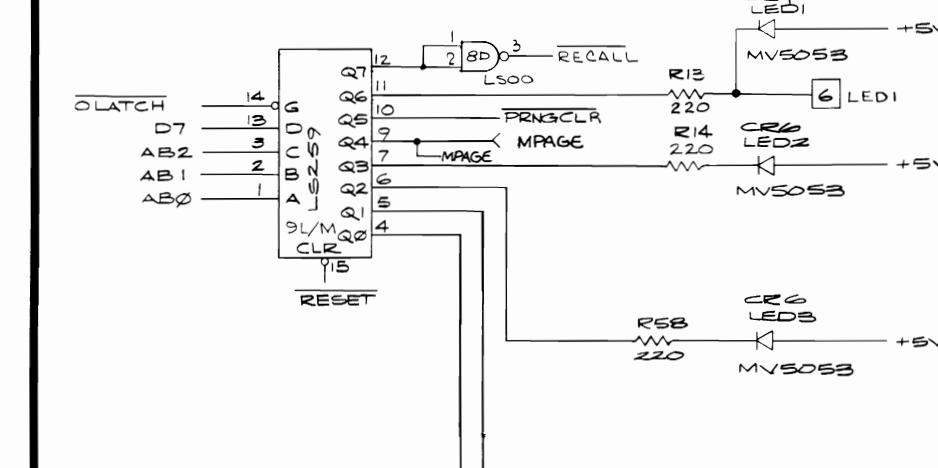
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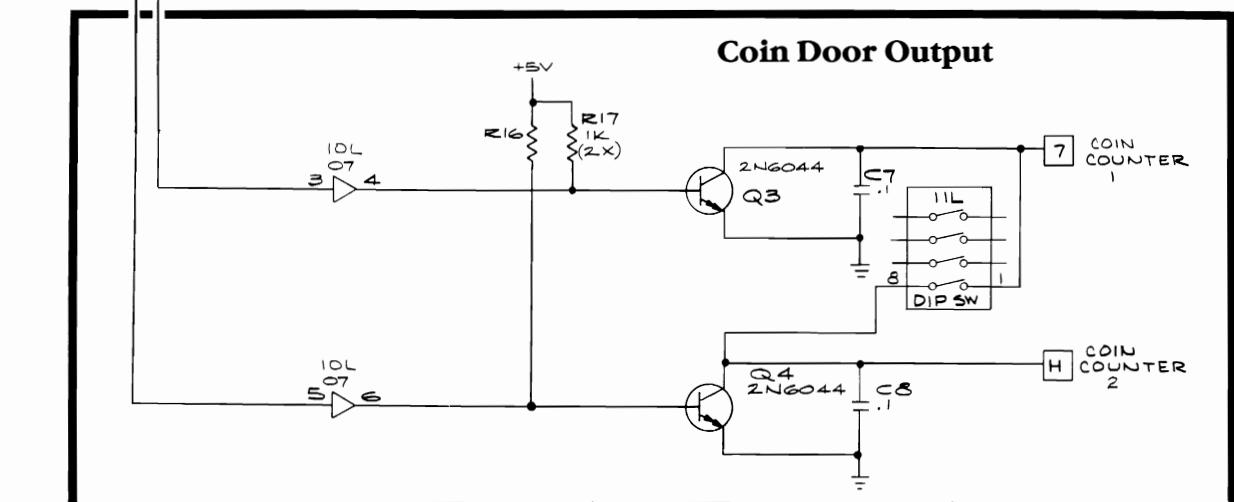
Address Decoders



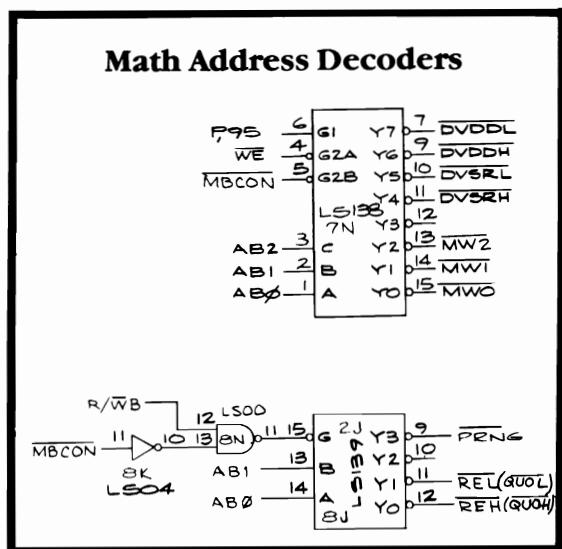
Test and Control Signals



Coin Door Output



Math Address Decoders



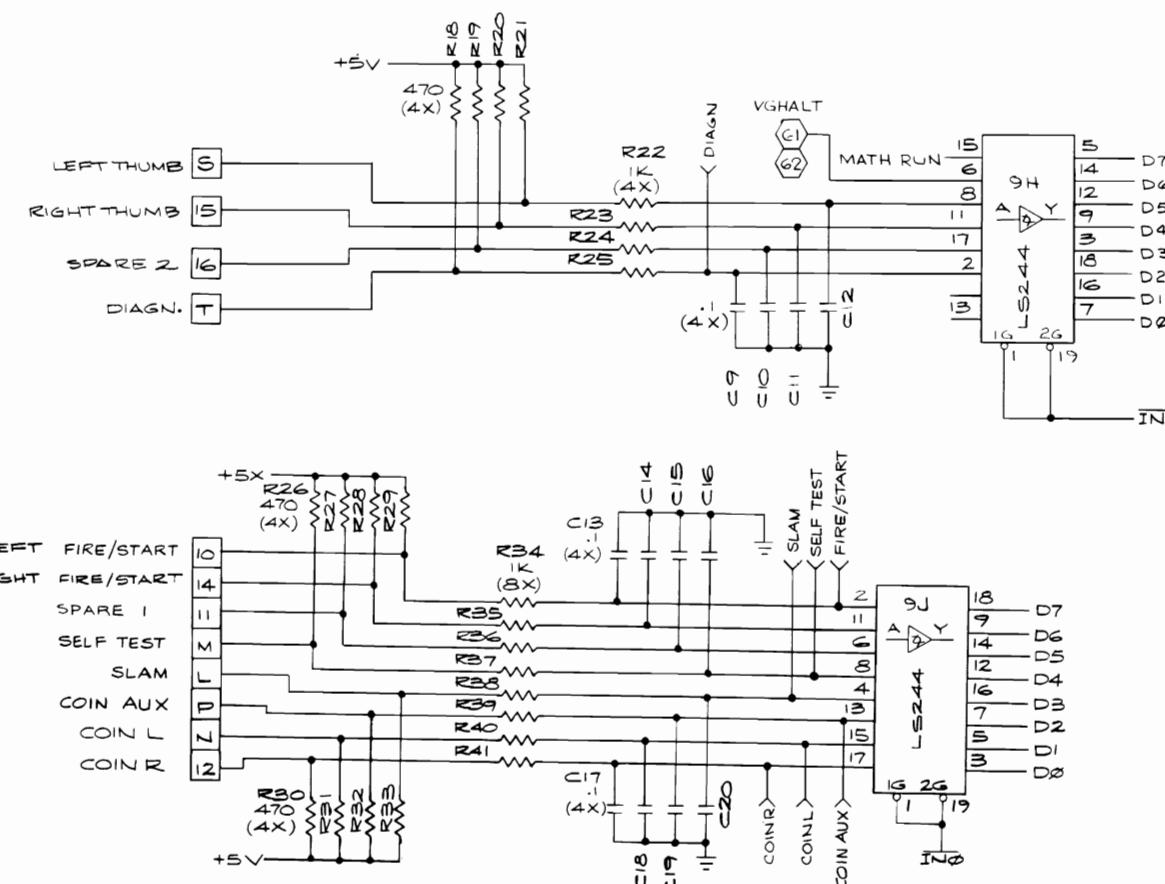
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STAR WARS Main PCB

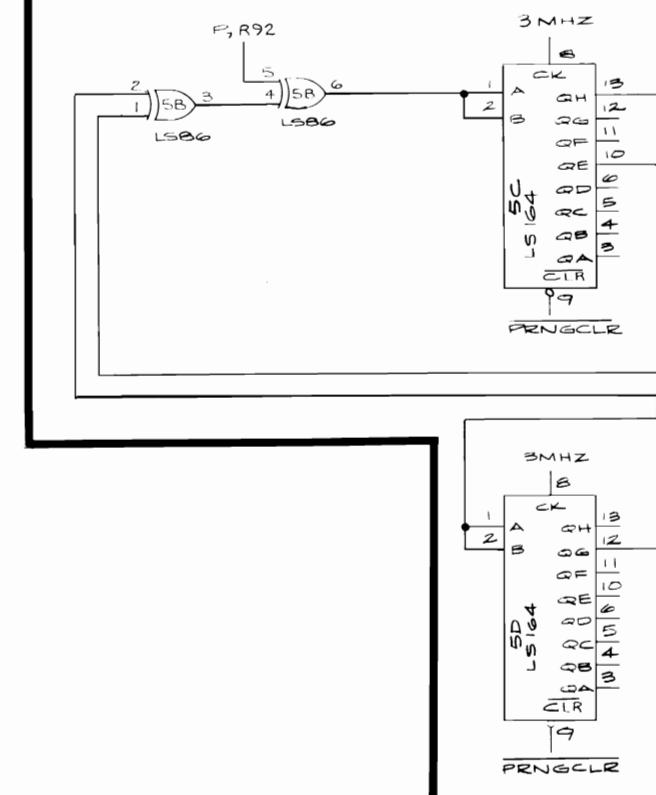


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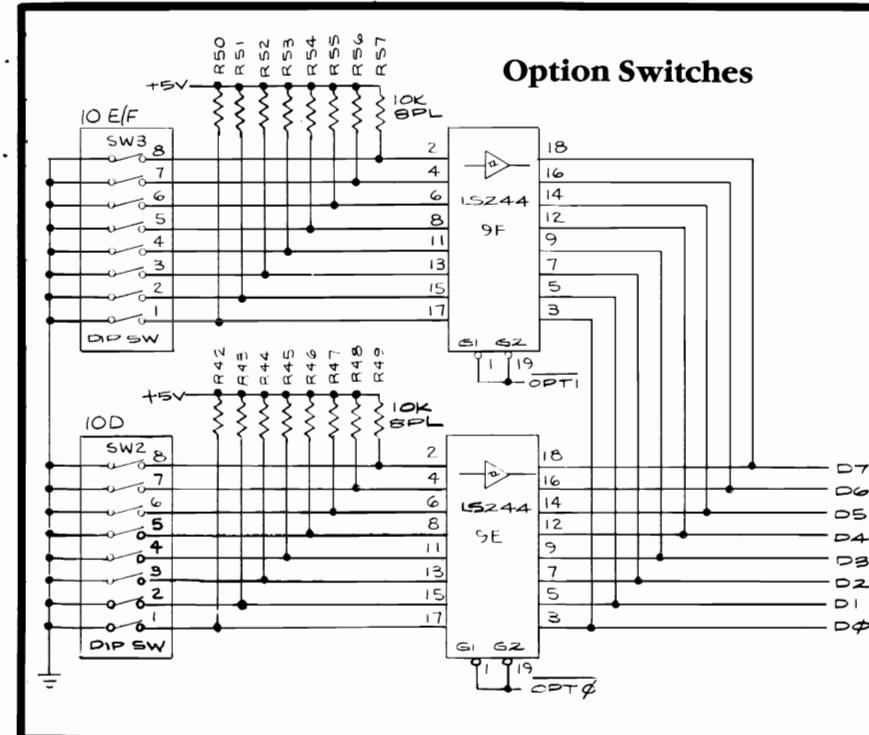
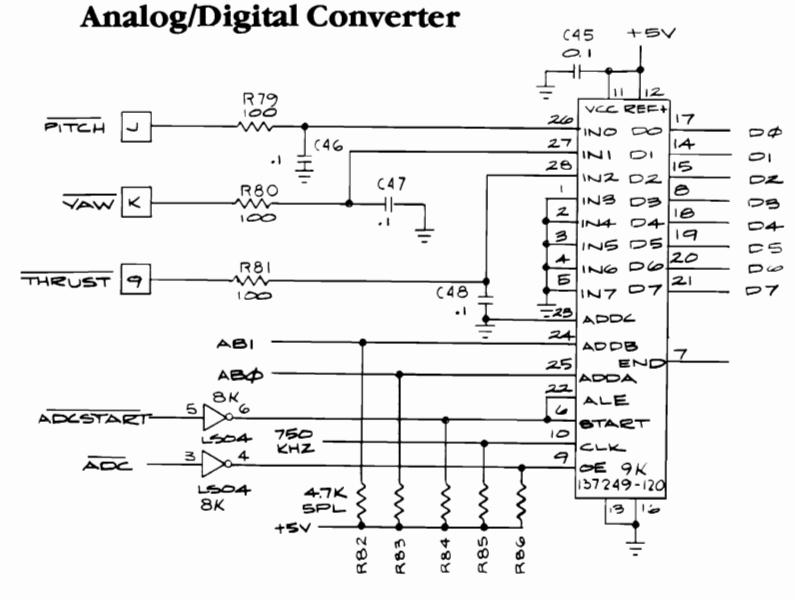
Coin Door and Control Panel Input



Pseudo-Random Number Generator



Analog/Digital Converter

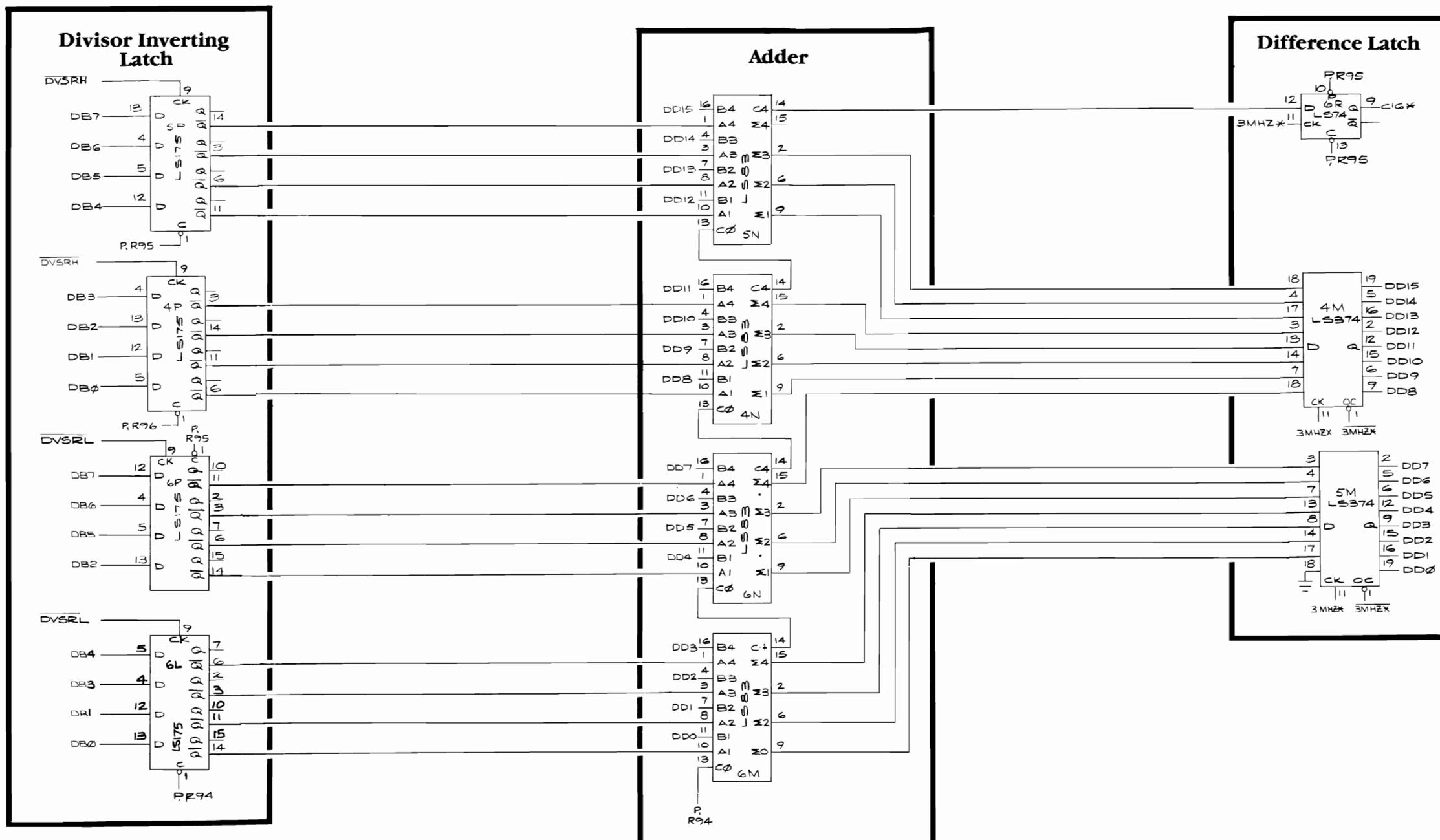


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STAR WARS Main PCB



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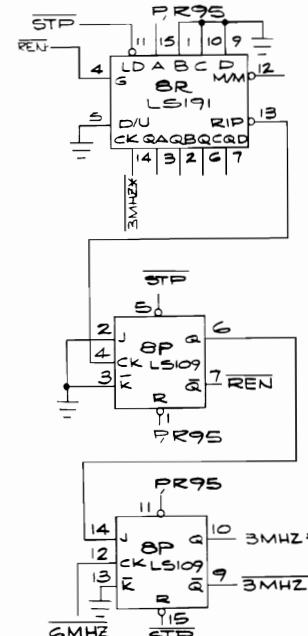
STAR WARS Main PCB



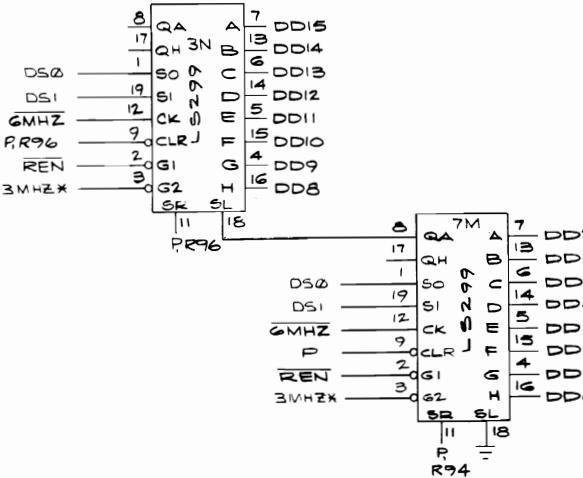
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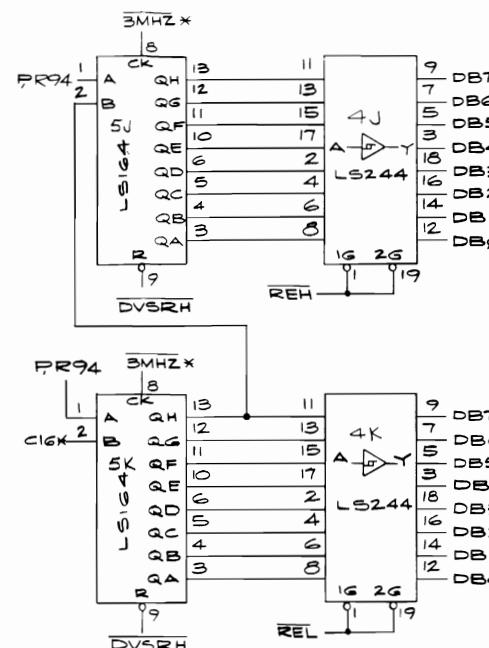
Divider Clock Generator



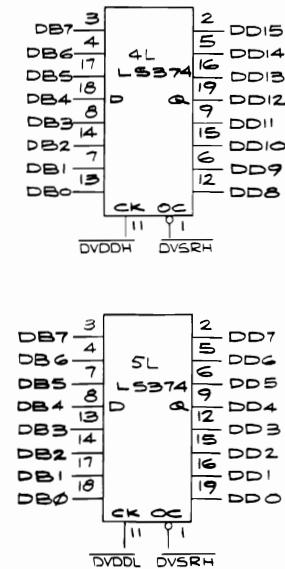
Dividend Shift Register



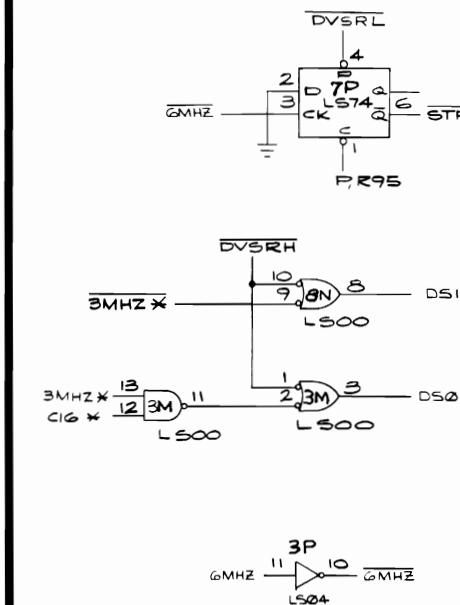
Quotient Shift Register



Dividend Latch



Divider Control



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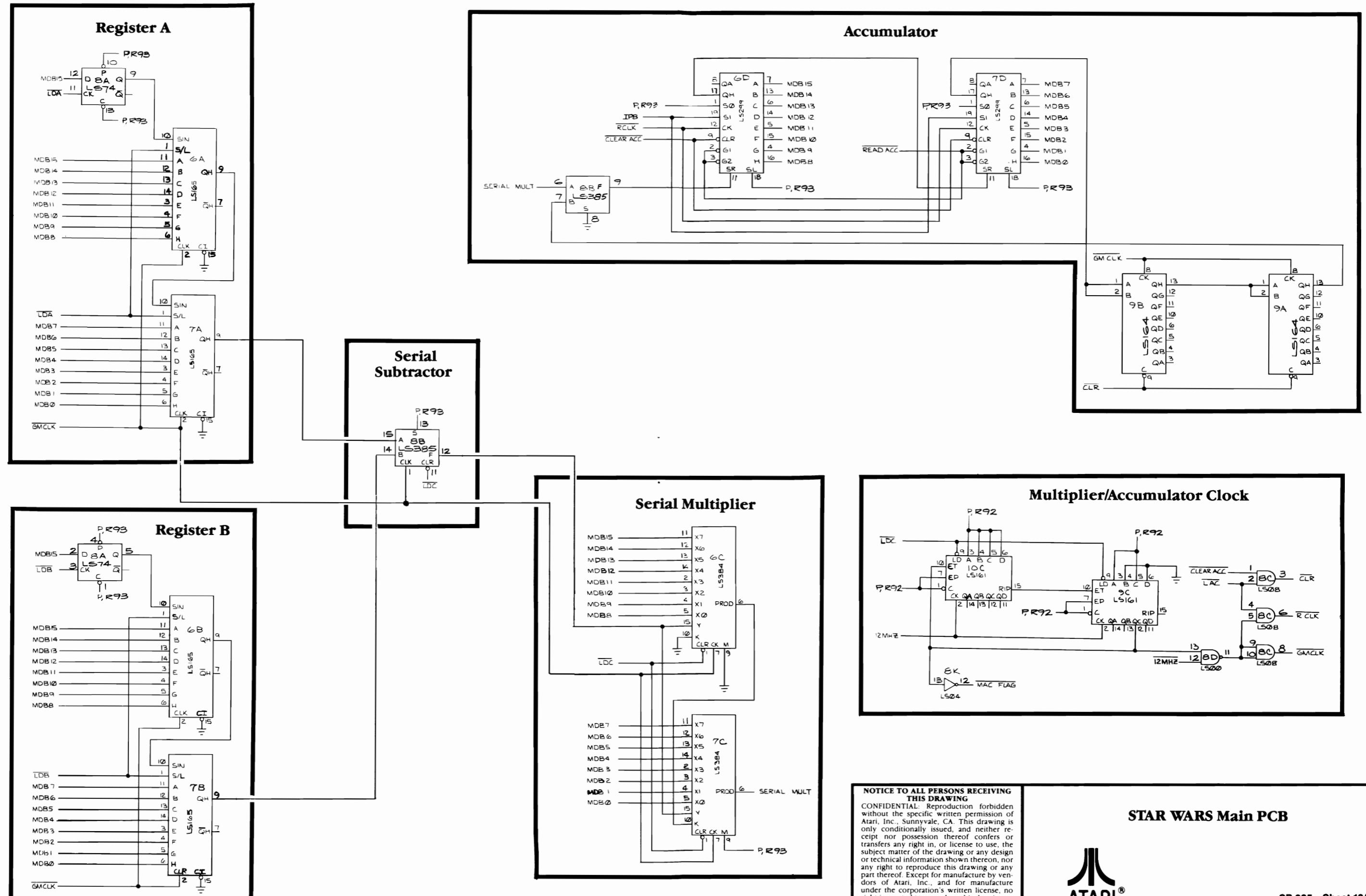
STAR WARS Main PCB



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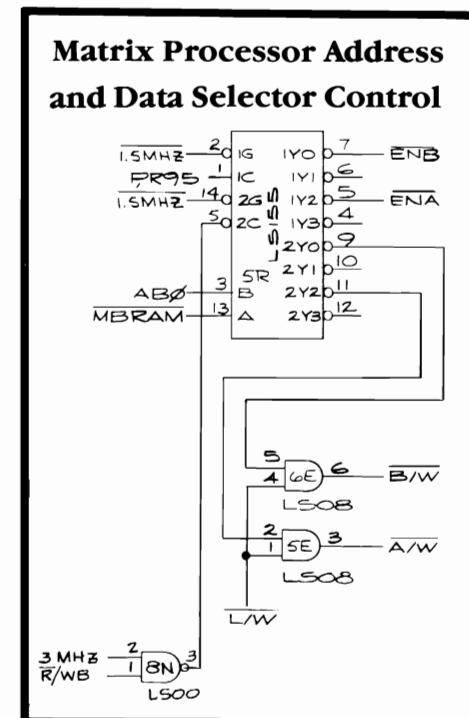
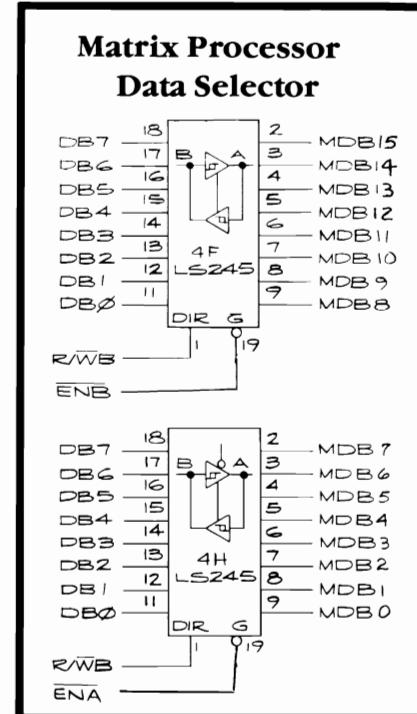
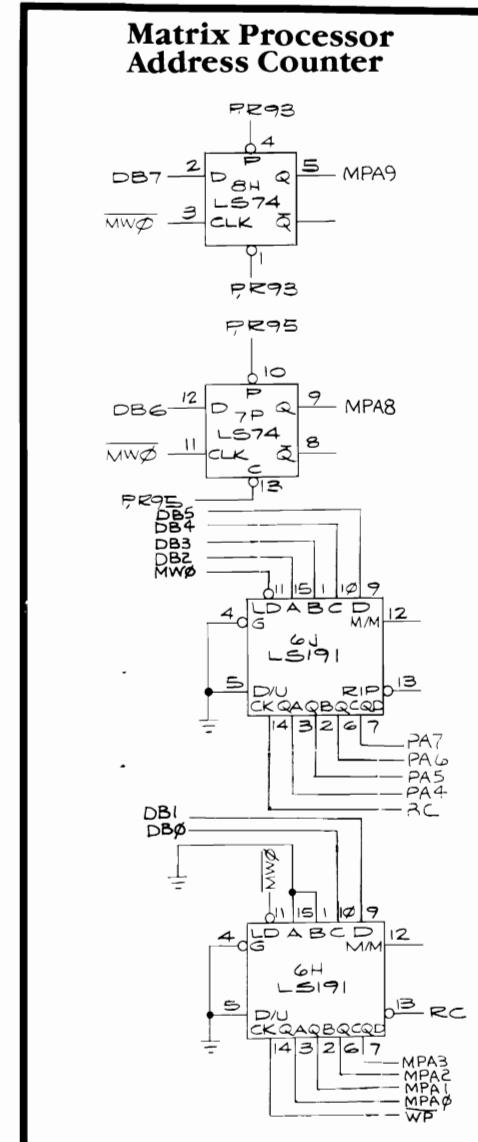
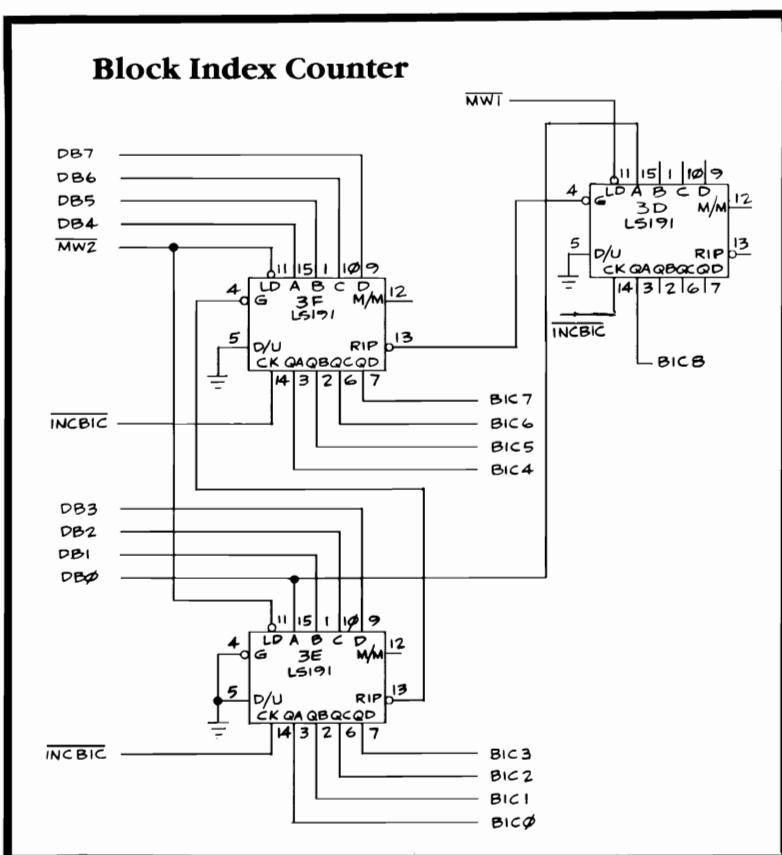
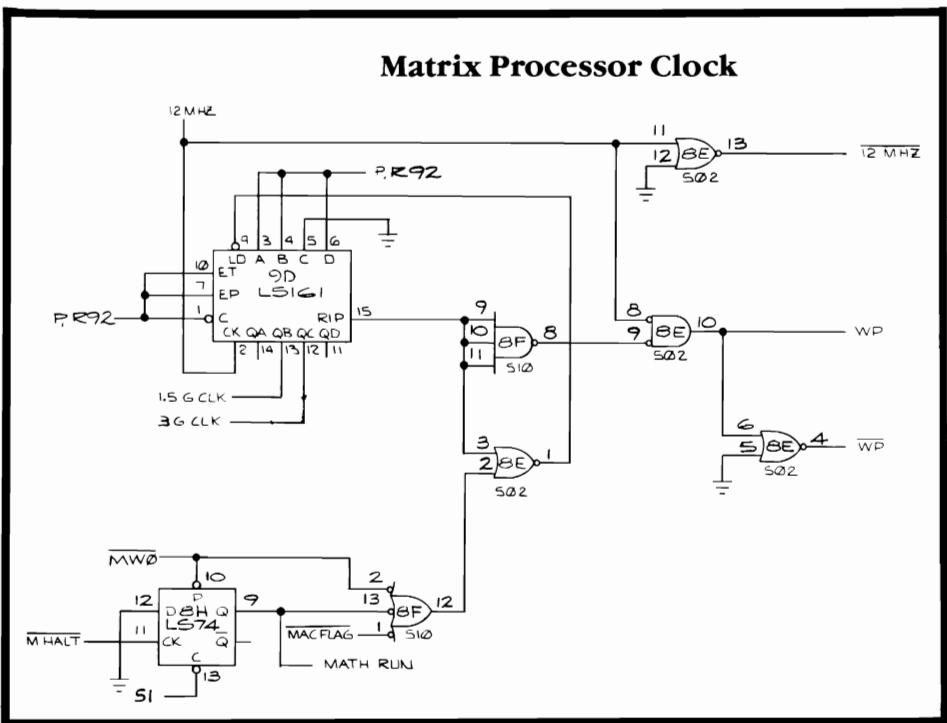
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STAR WARS Main PCB



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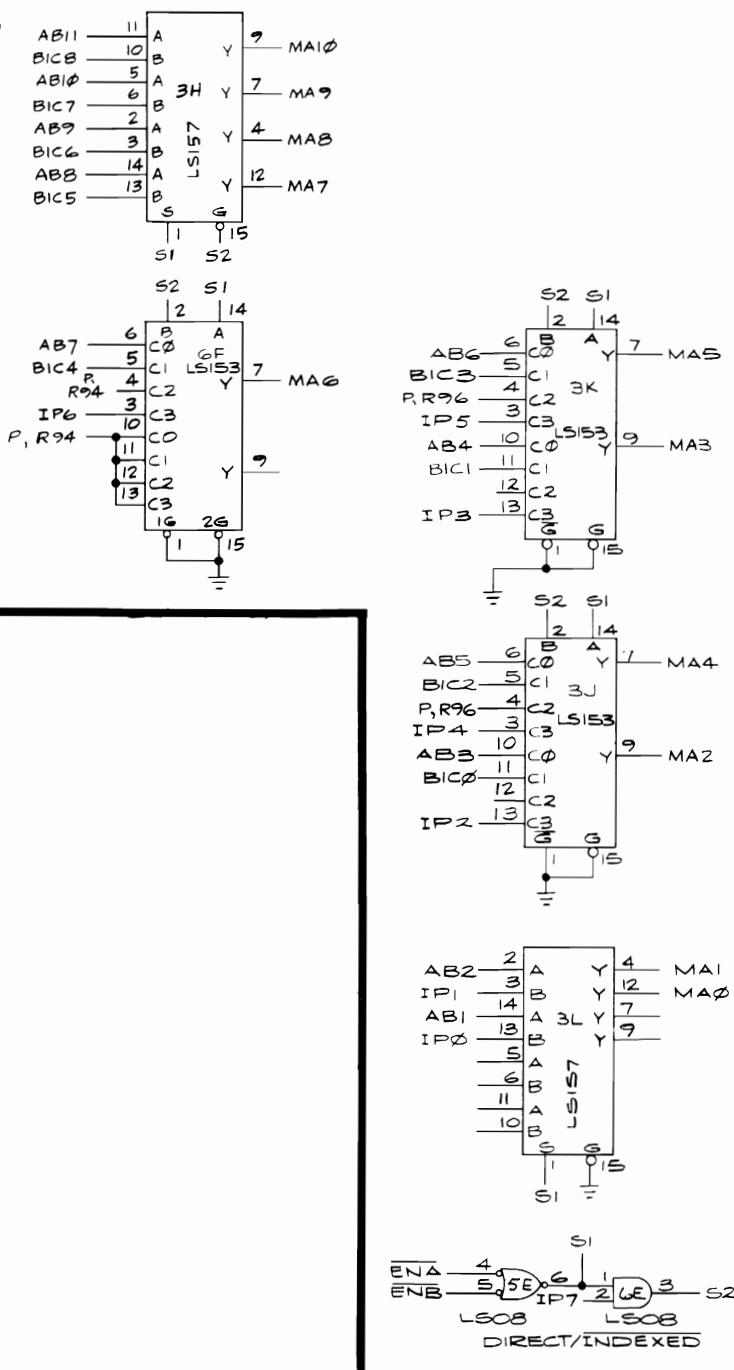
STAR WARS Main PCB



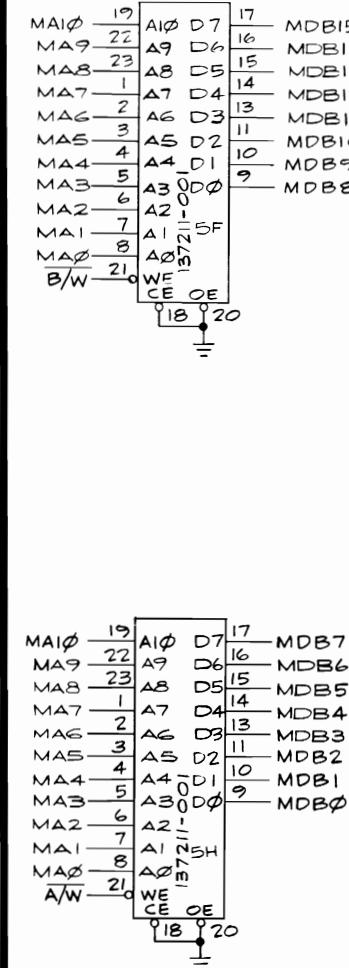
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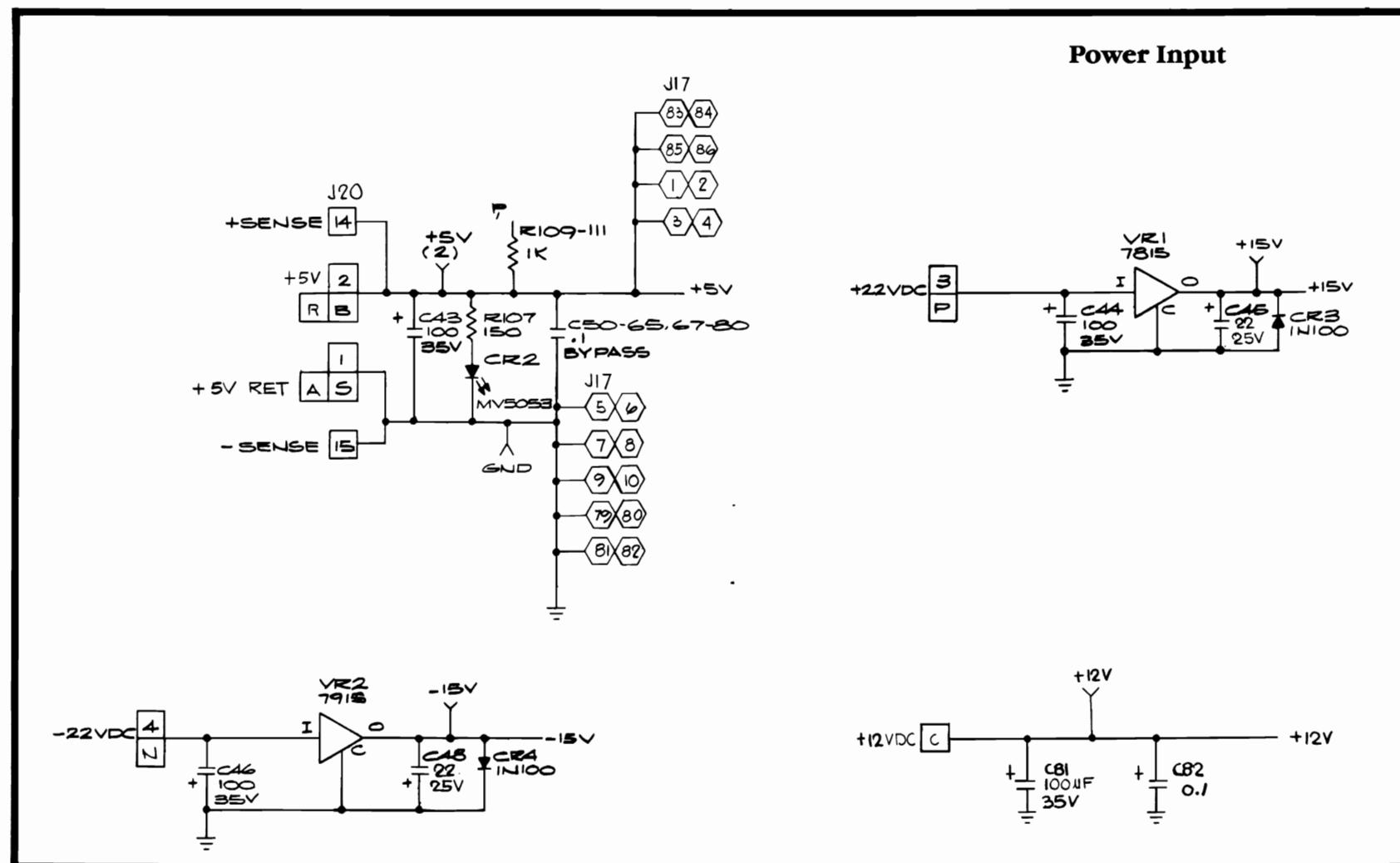
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Matrix Processor Address Selector



Matrix Processor RAM





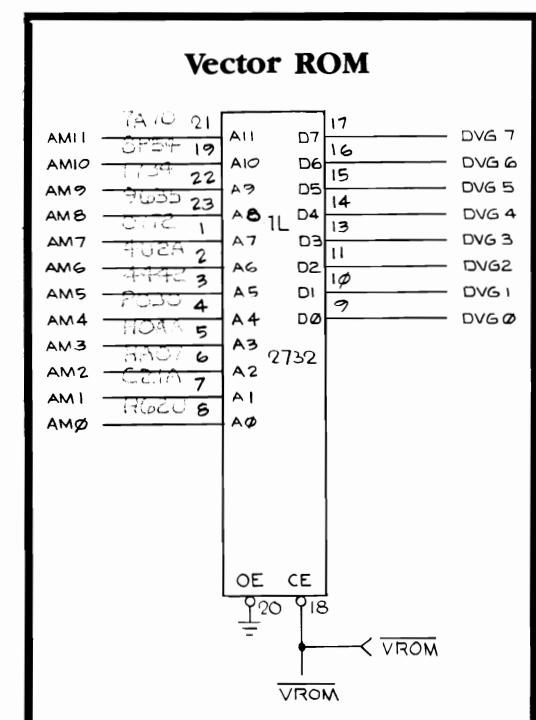
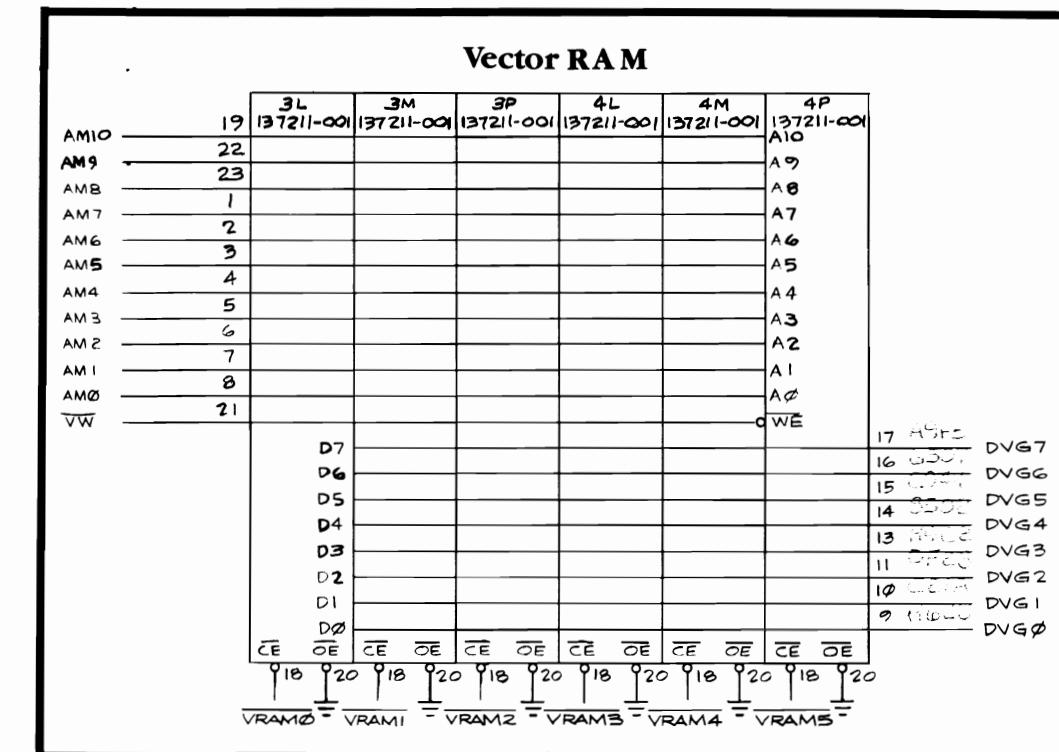
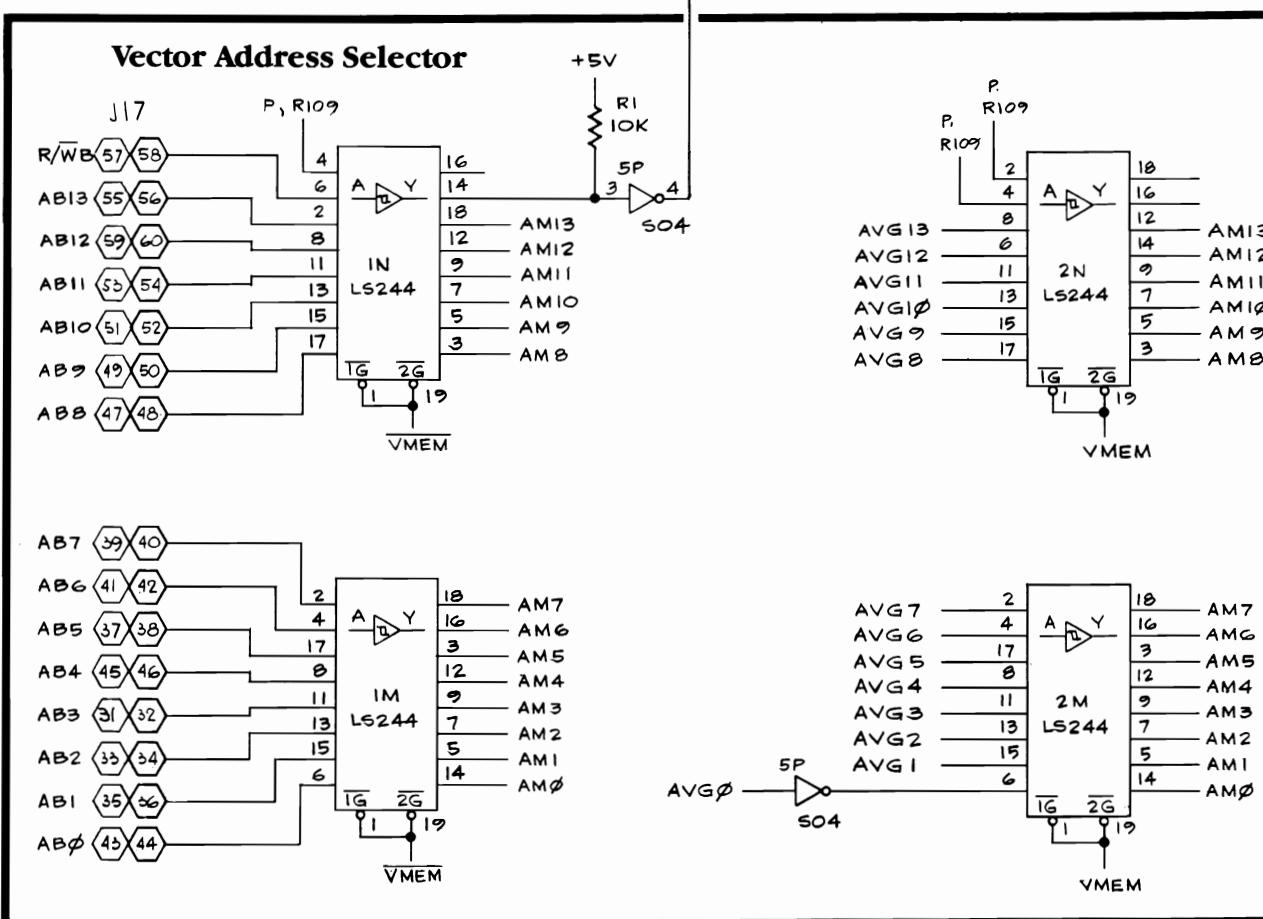
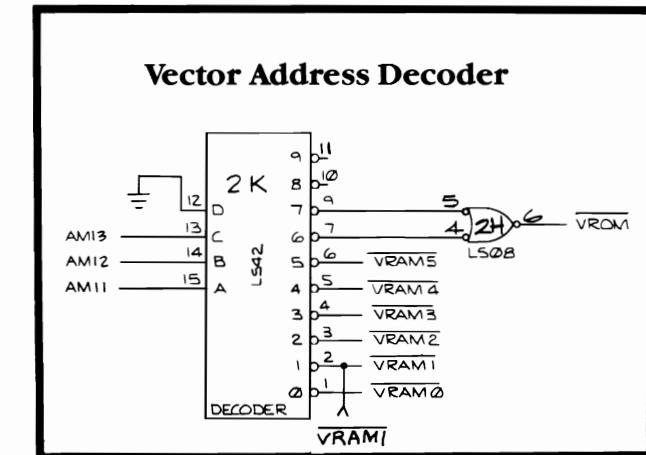
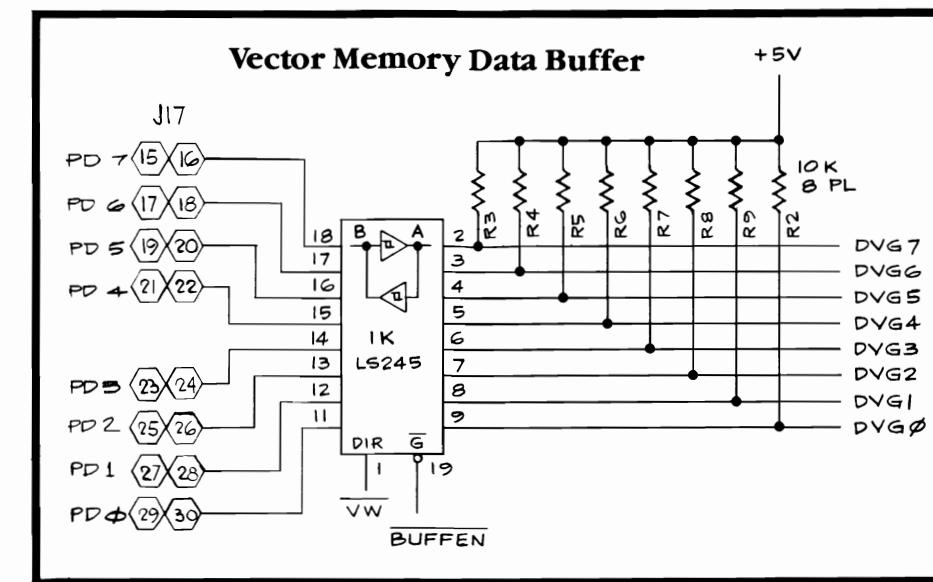
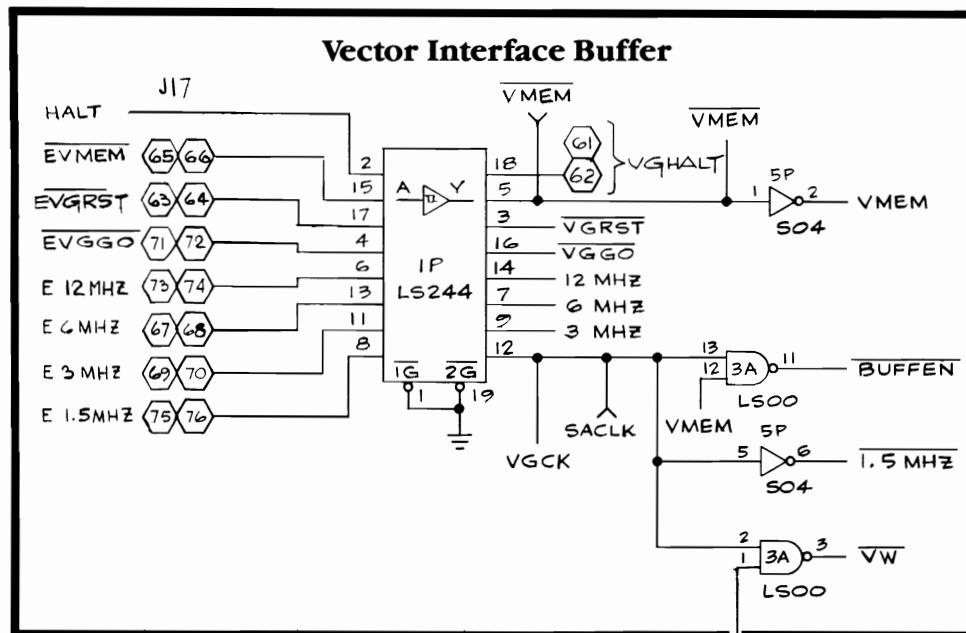
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STAR WARS Analog Vector-Generator PCB



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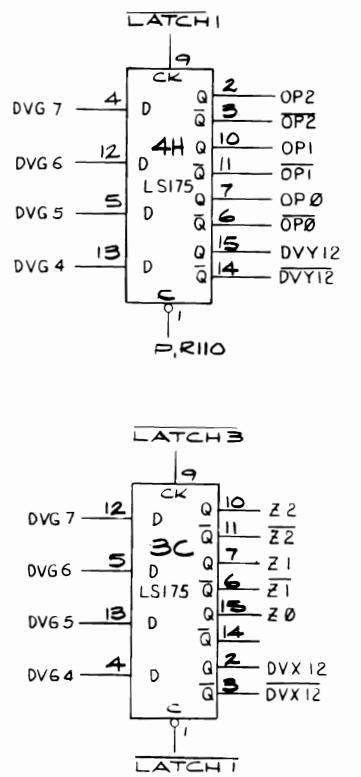


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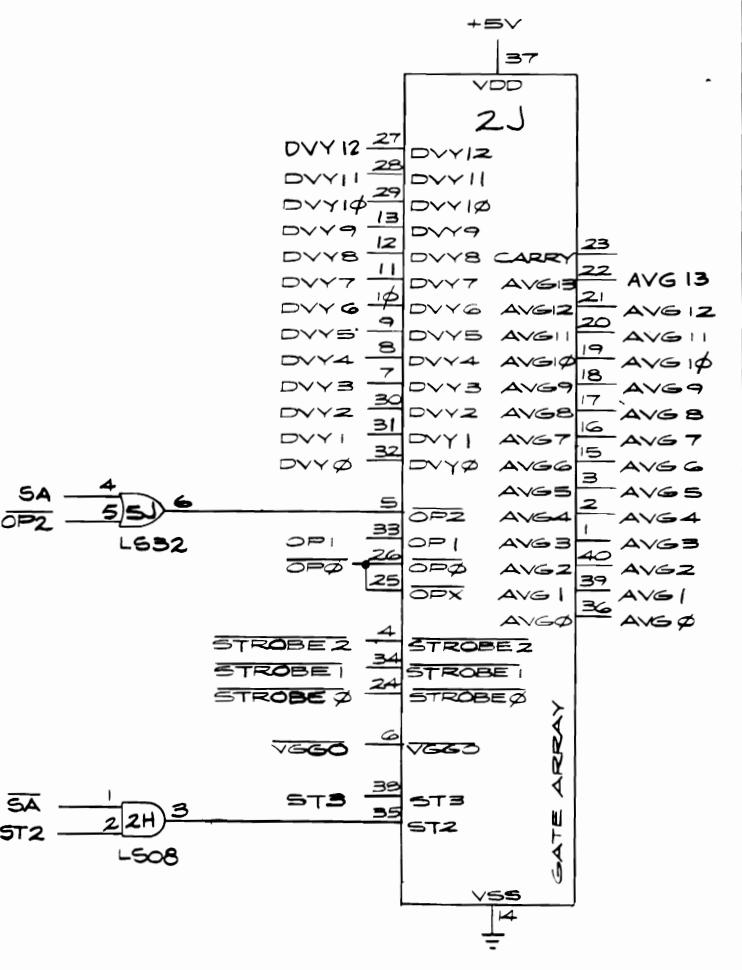
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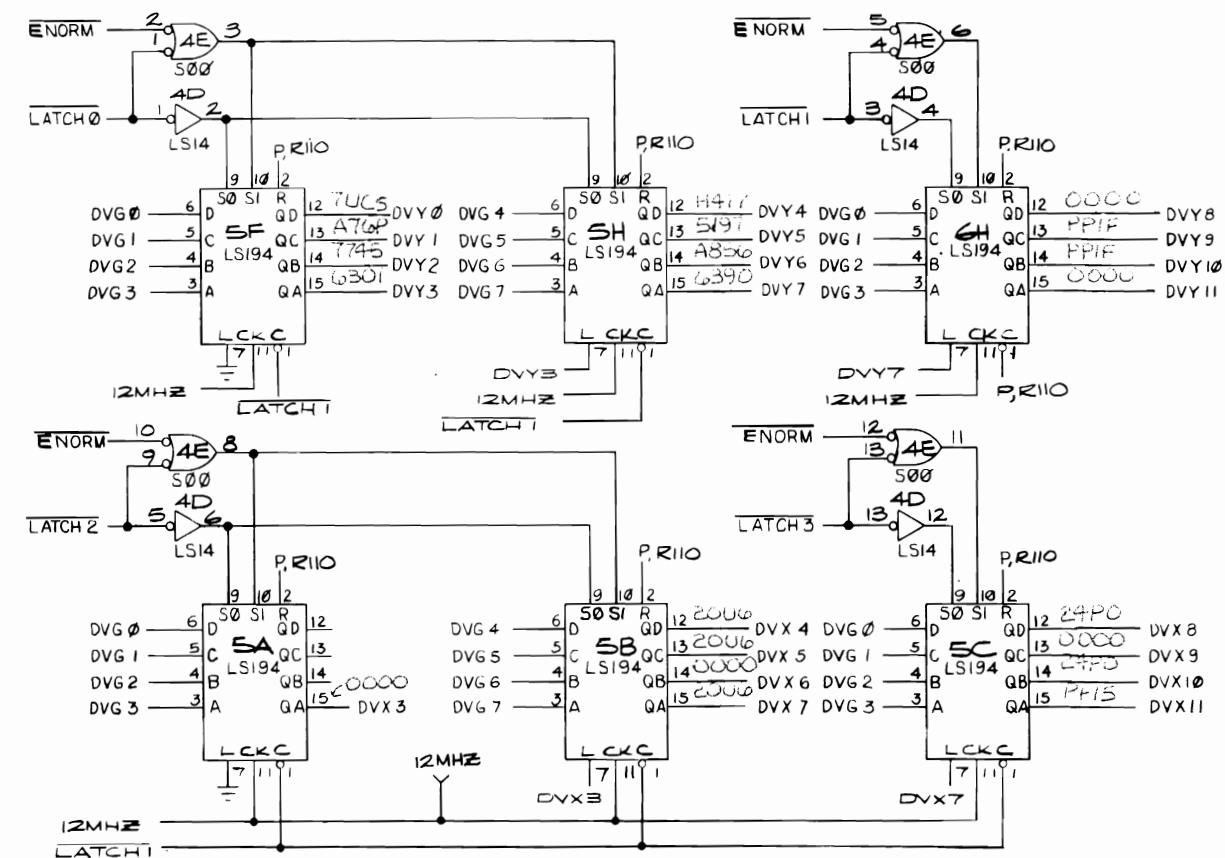
Op Code and Intensity Latches



Address Controller



Vector Data Shifters



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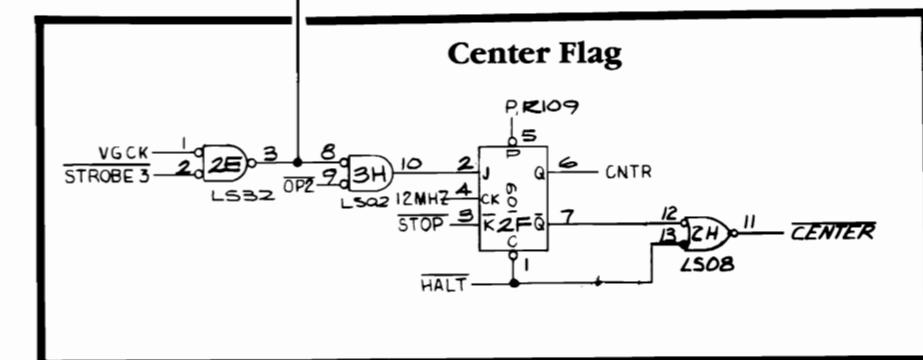
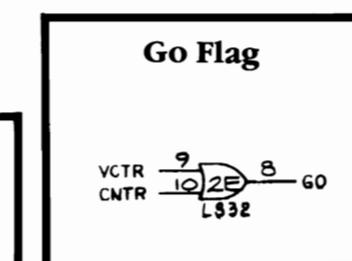
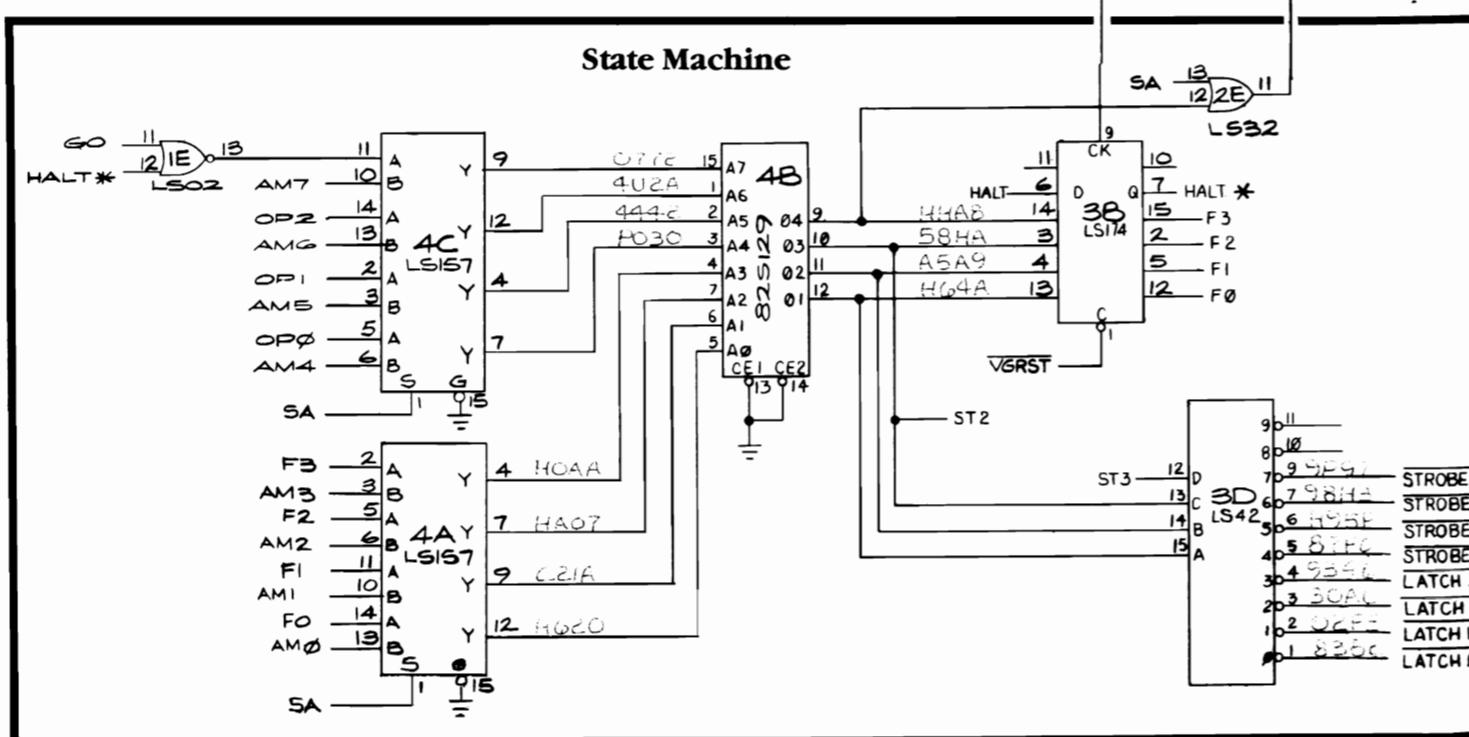
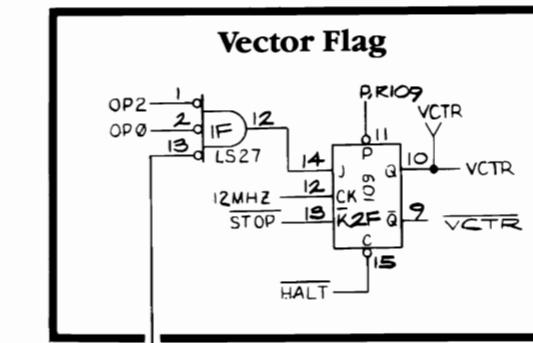
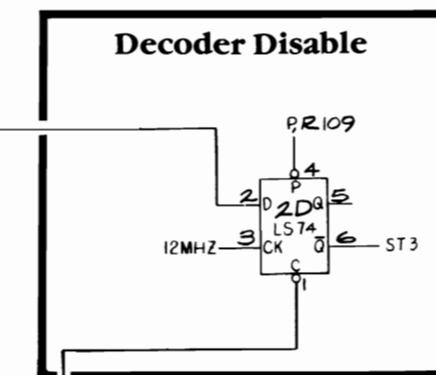
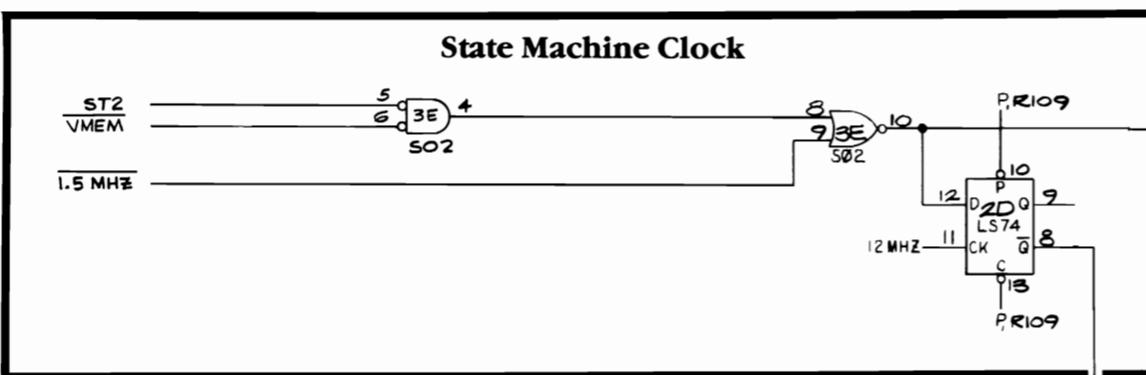
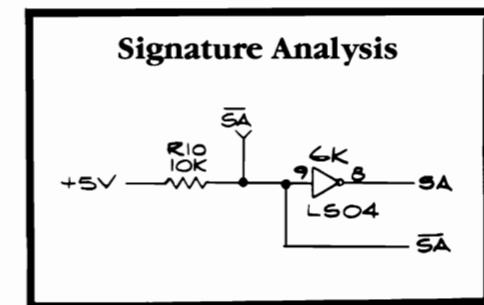
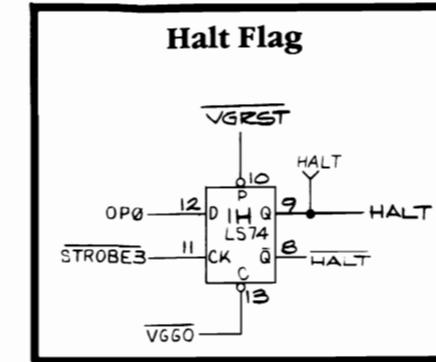
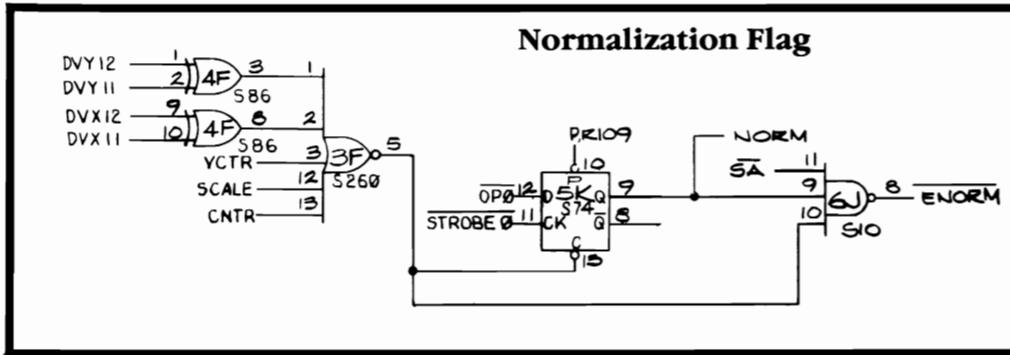
STAR WARS Analog Vector-Generator PCB



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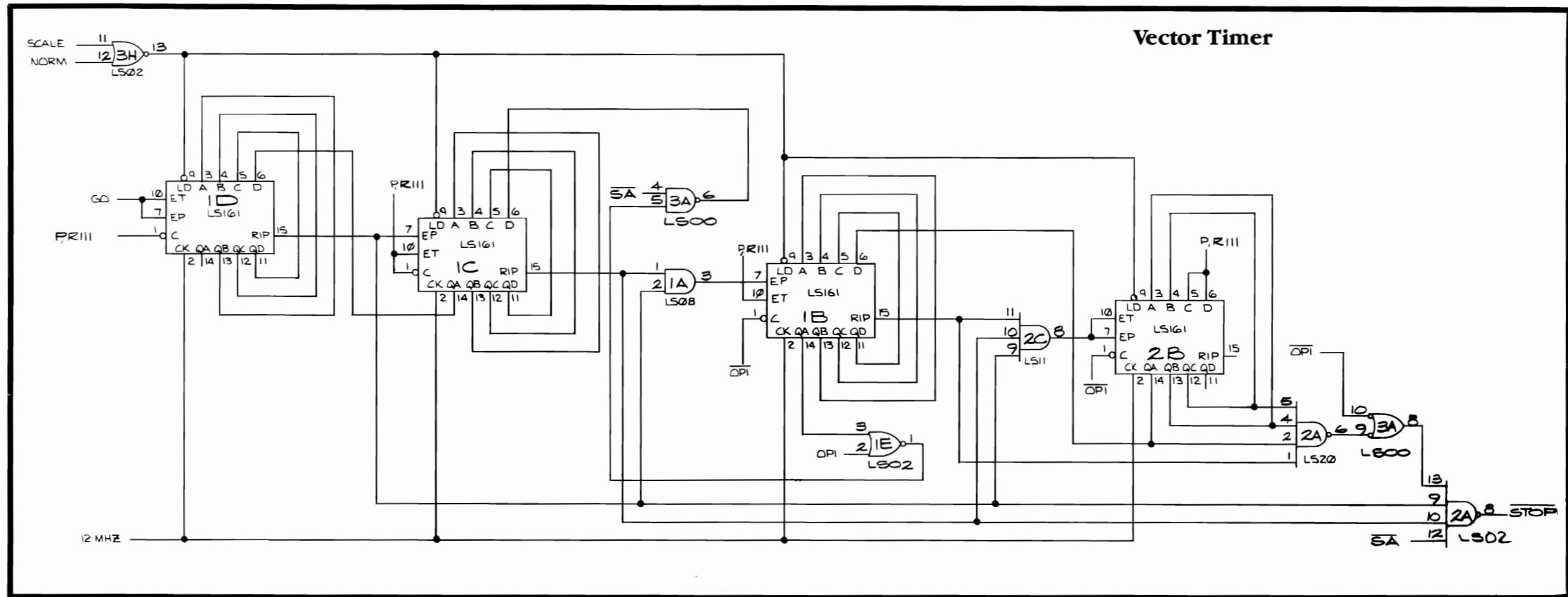


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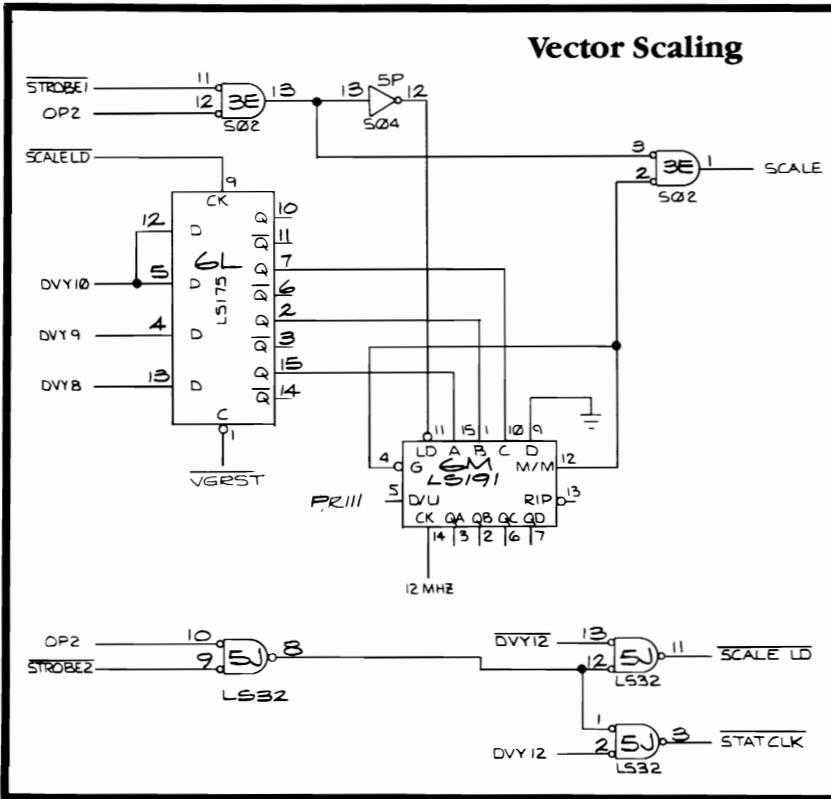
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Vector Timer



Vector Scaling



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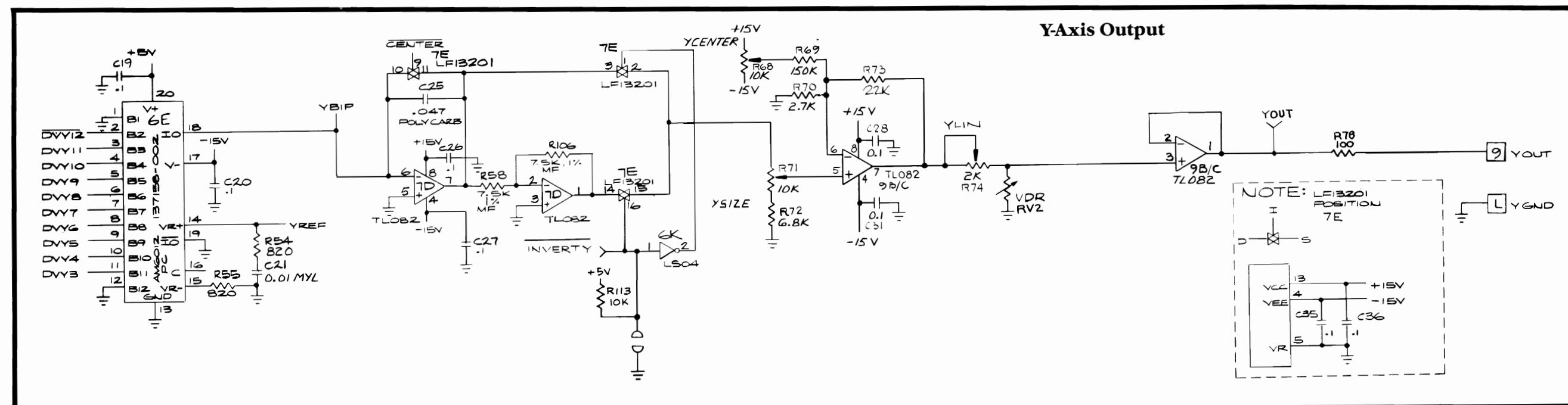
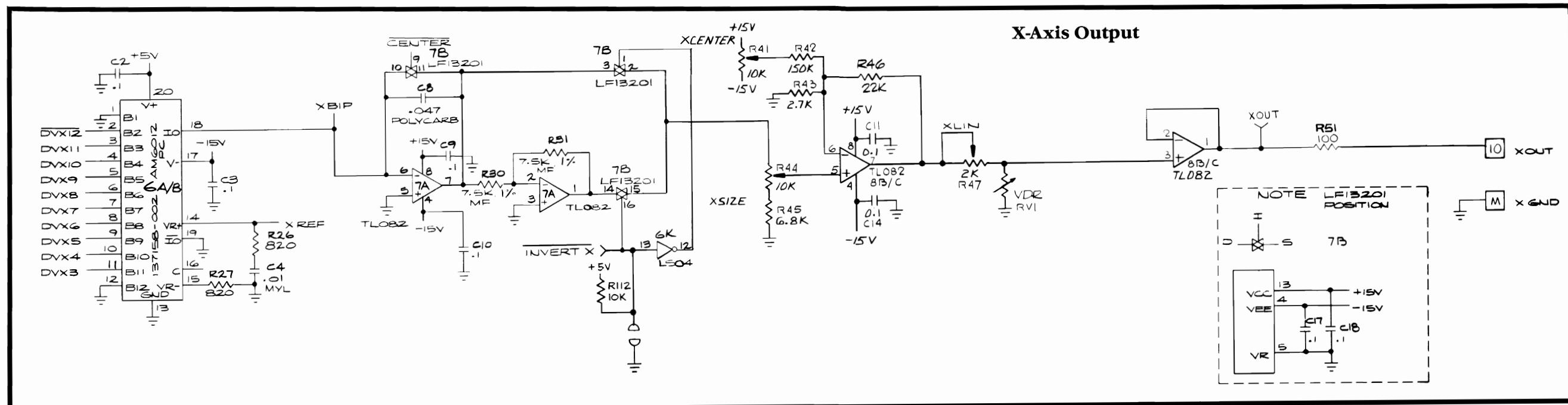
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X and Y Video Adjustments

Perform the following procedure to adjust the display size, centering, and linearity:

1. Set the game to the Self-Test Mode and select the Cross-hatch Pattern Display as described in Chapter 2 of the game manual.
2. Adjust XCENTER (R41) and YCENTER (R68) so that the crosshatch pattern is centered on the screen.
3. Adjust XSIZE (R44) and YSIZE (R71) so that the cross-hatch pattern covers the entire visible screen area.
4. Adjust XLIN (R47) and YLIN (R74) so that the diagonal lines are straight and the diamond-shaped squares are symmetrical. Since the linearity potentiometers change the size of the display, it may be necessary to repeat step 3 to obtain the correct display.

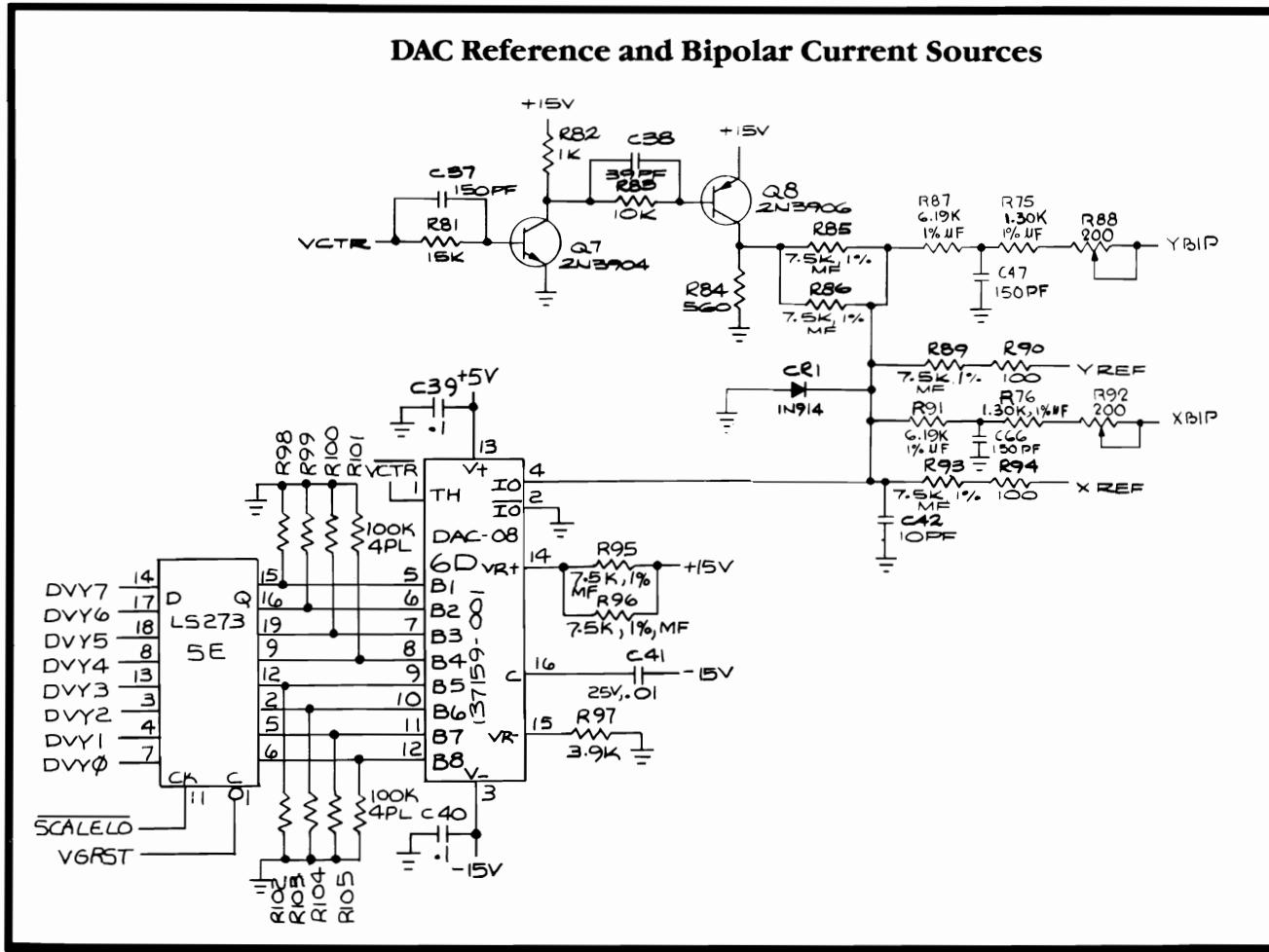
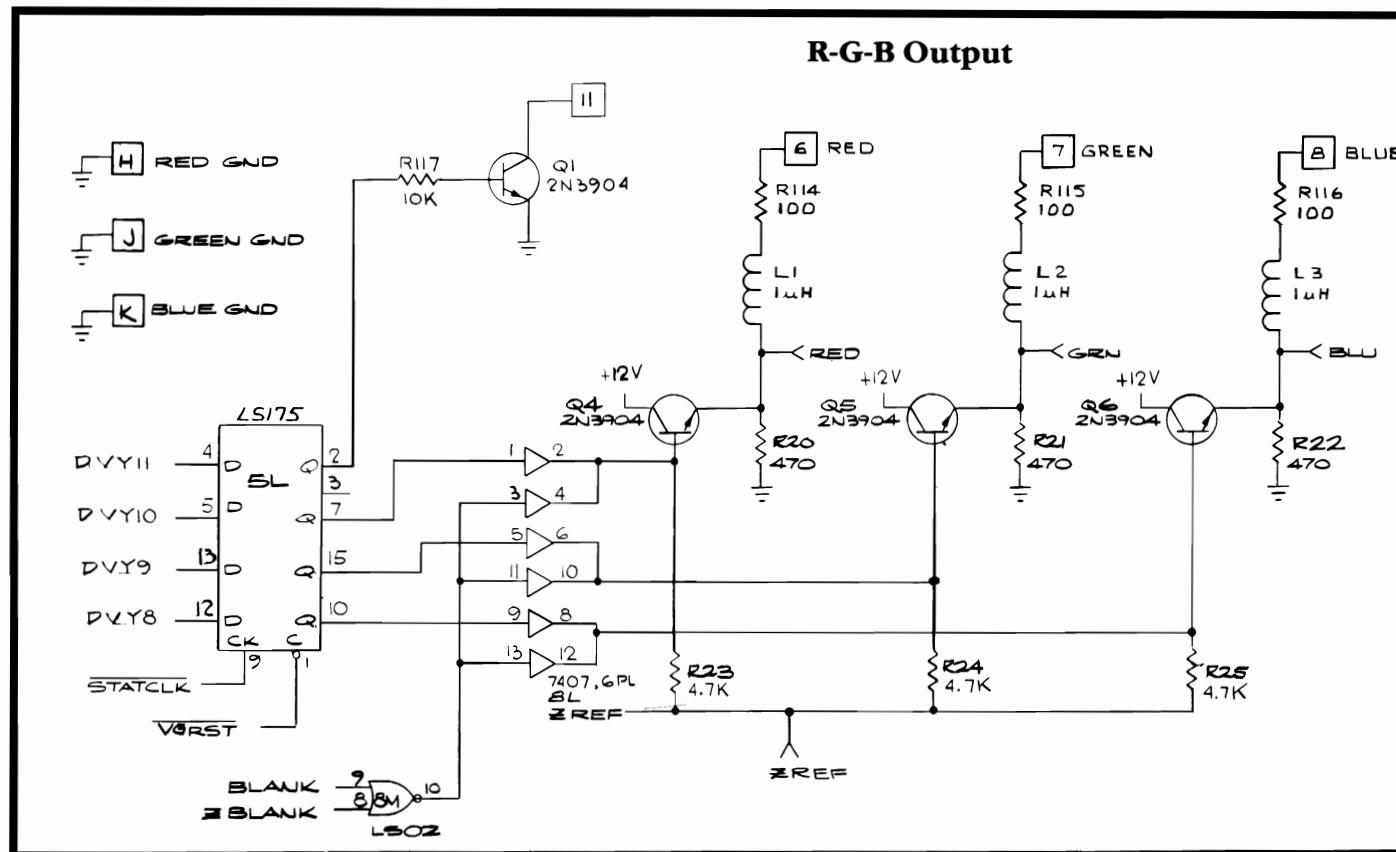
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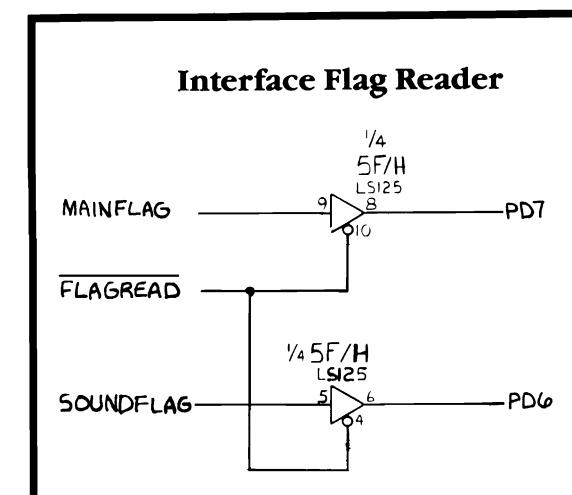
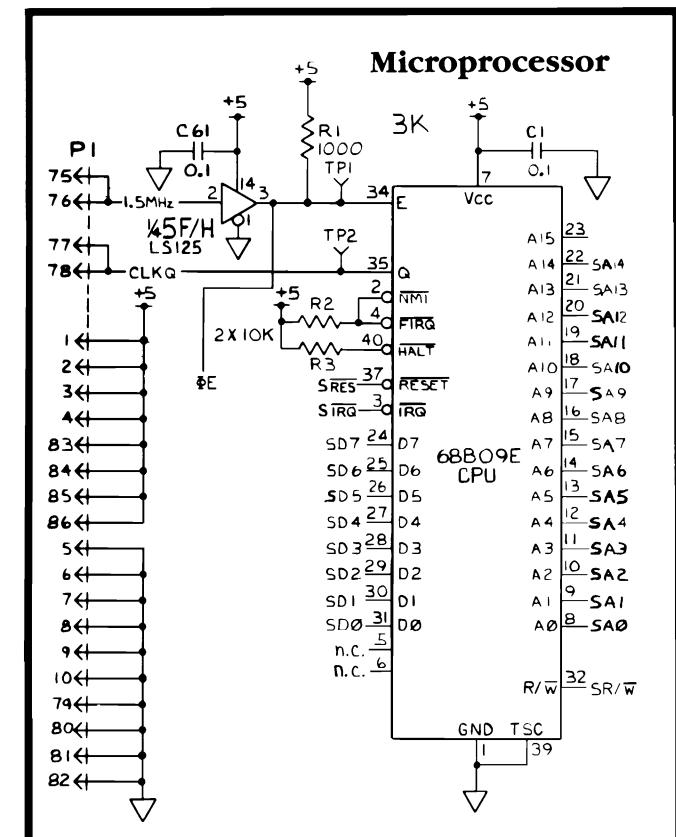
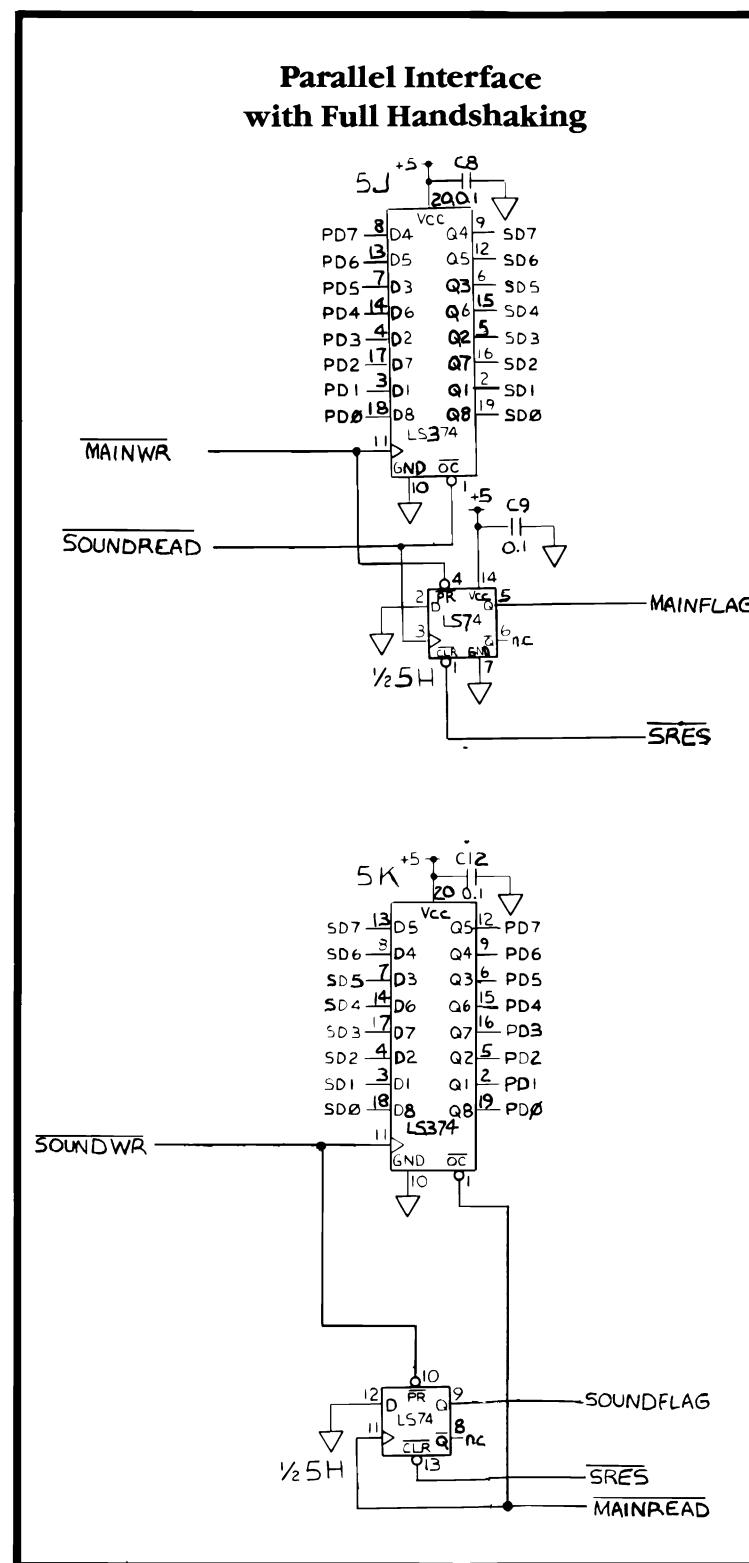
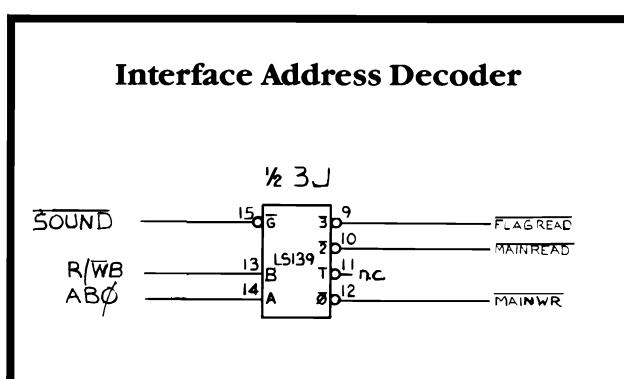
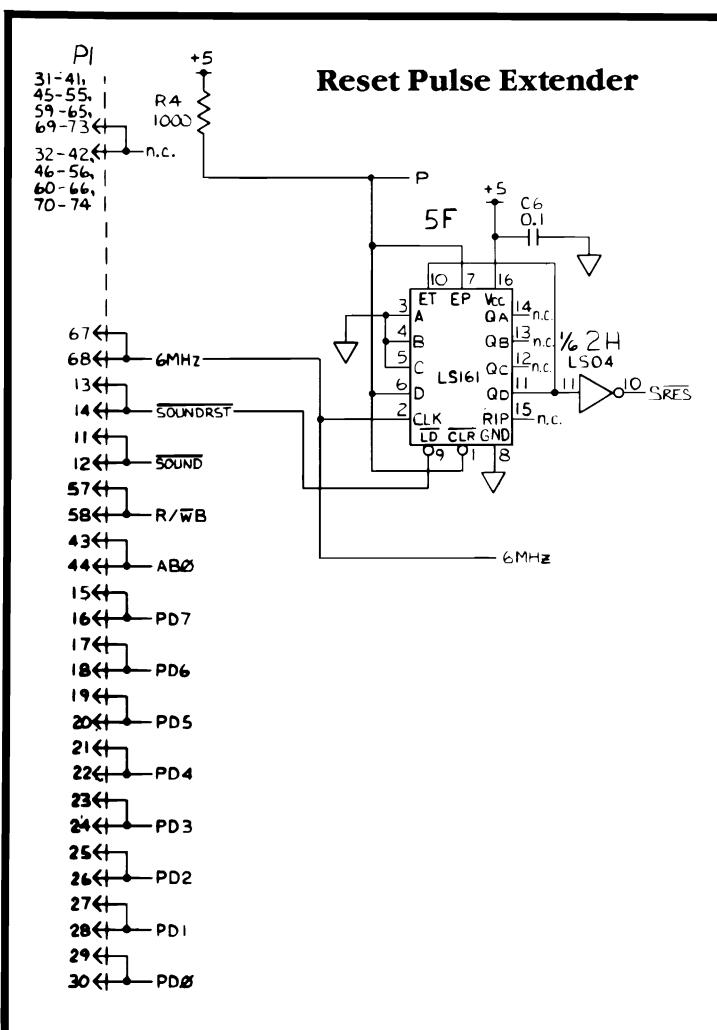
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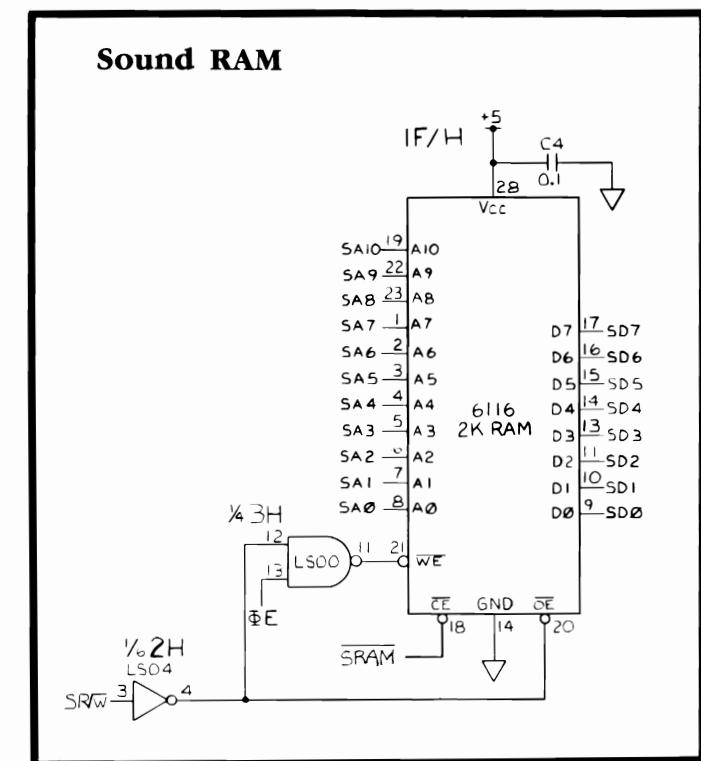
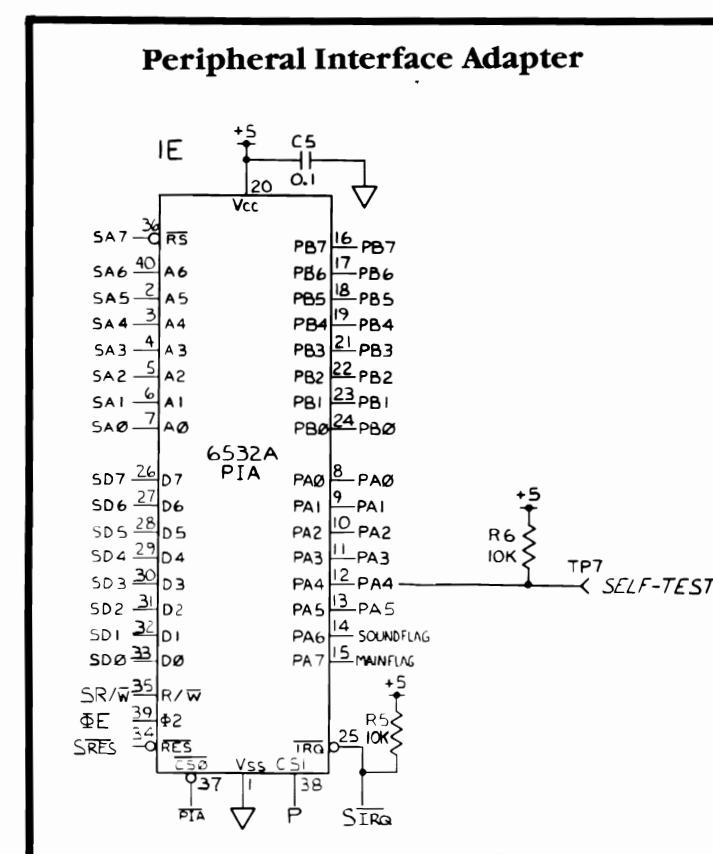
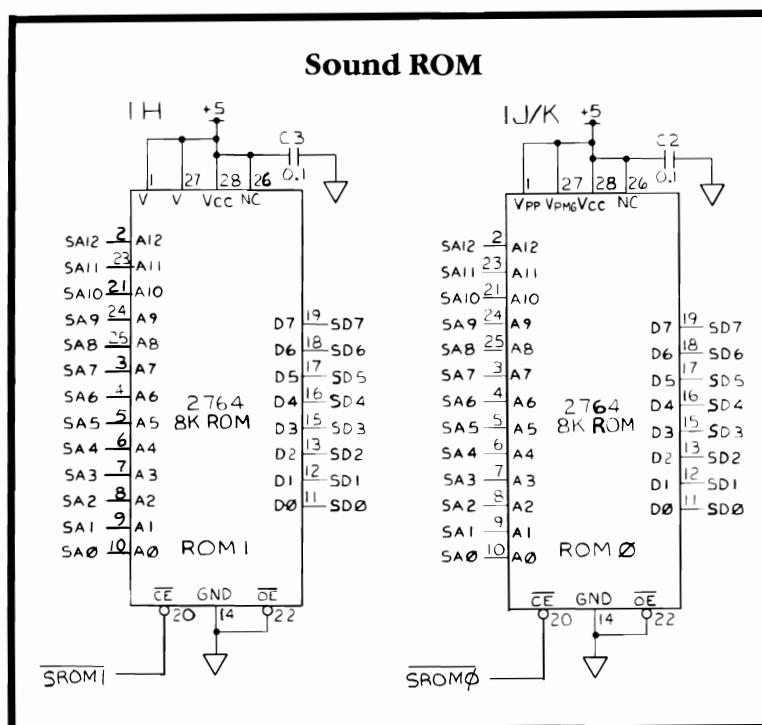
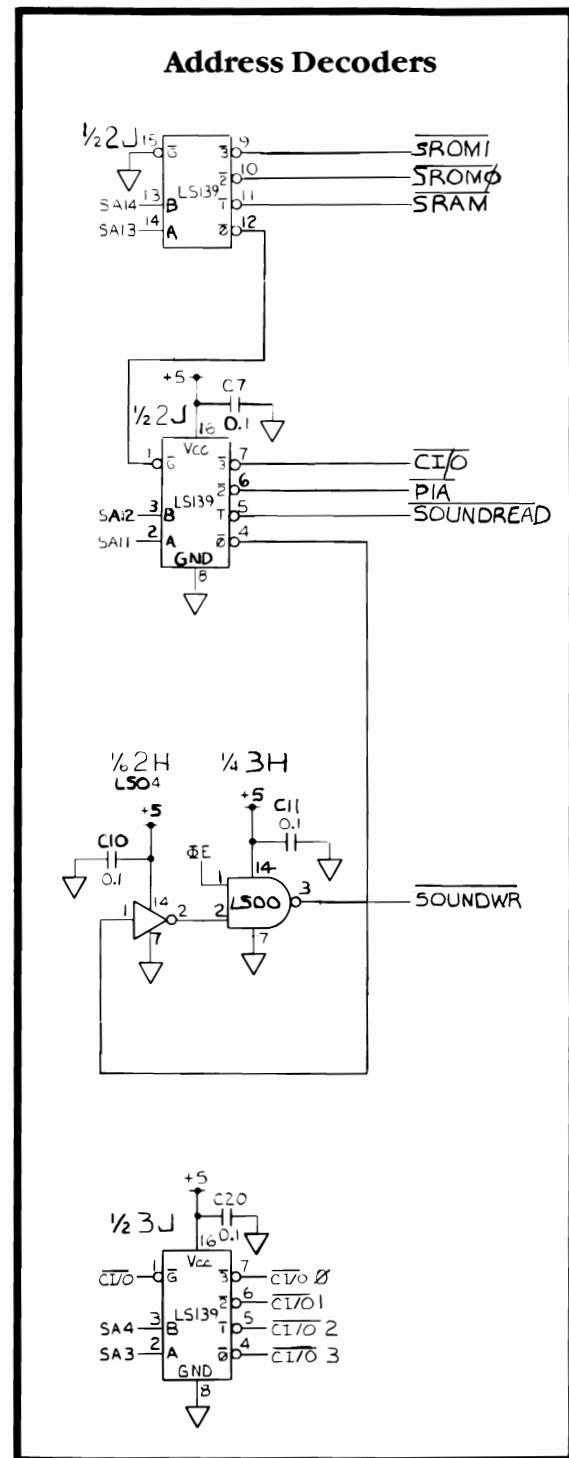


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STAR WARS Sound PCB





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STAR WARS Sound PCB

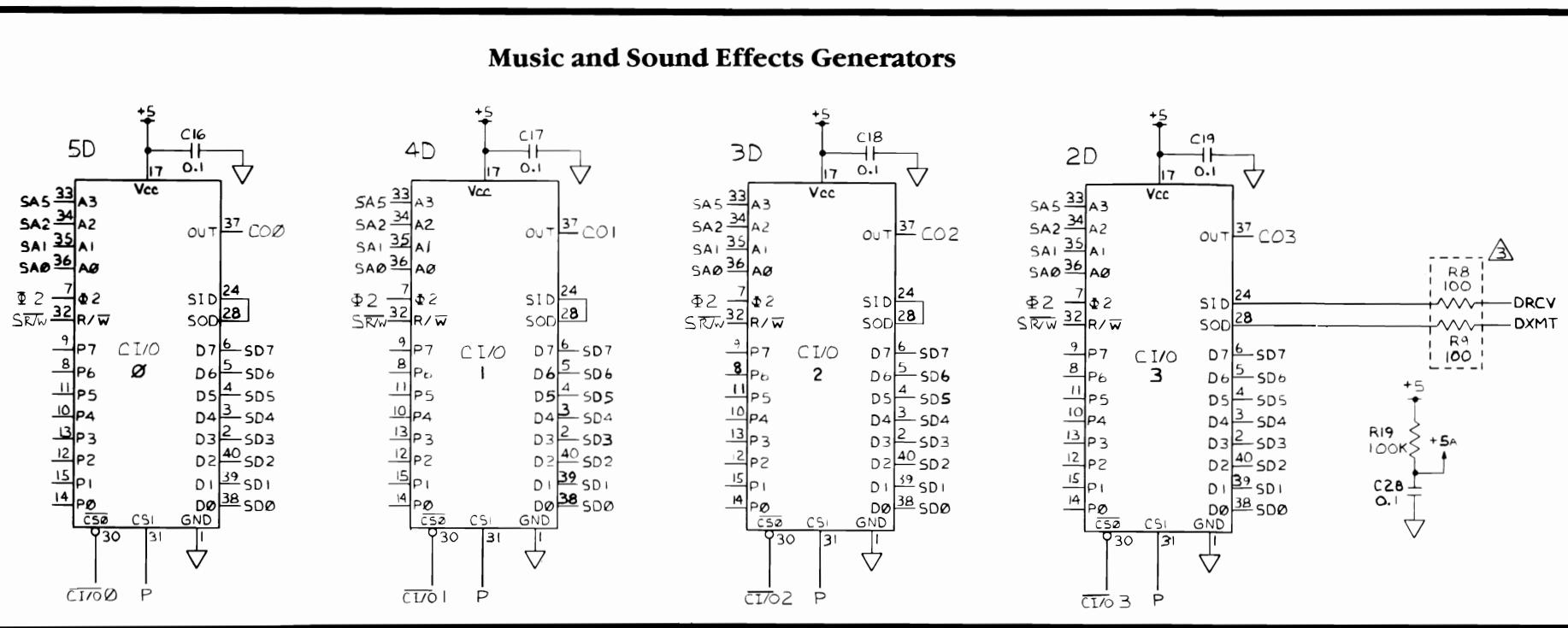


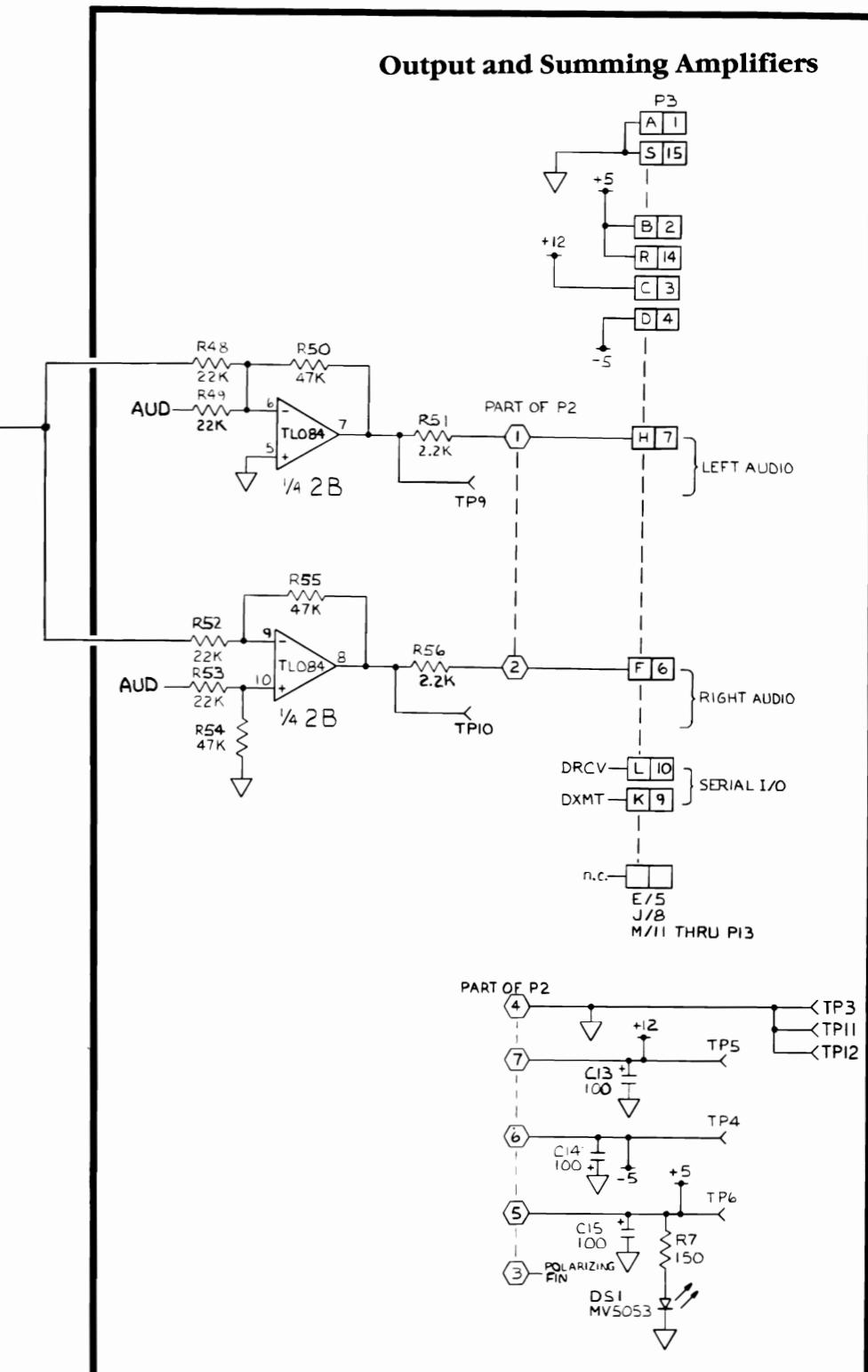
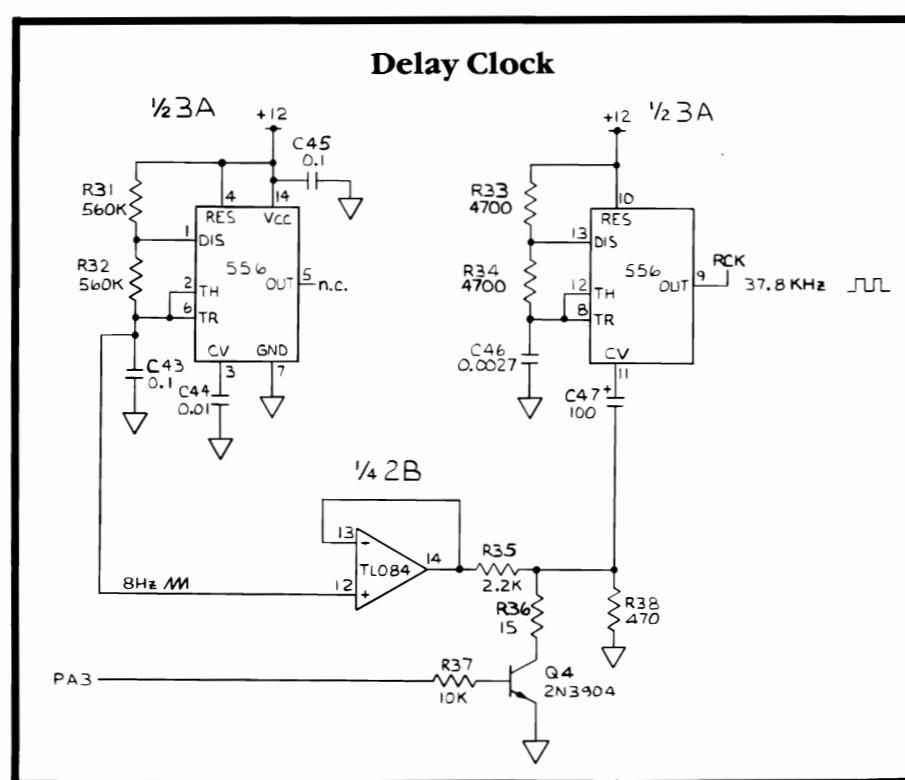
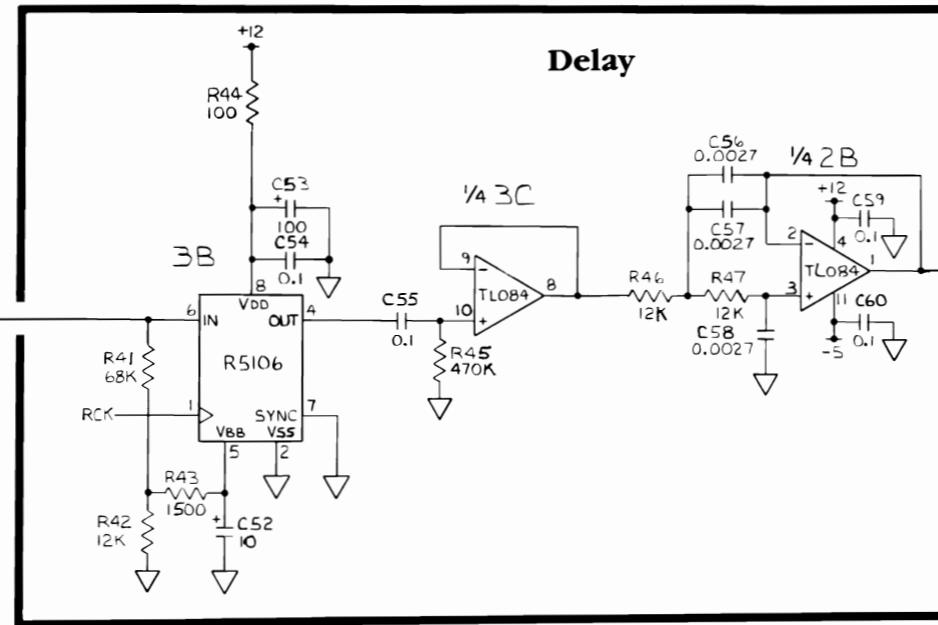
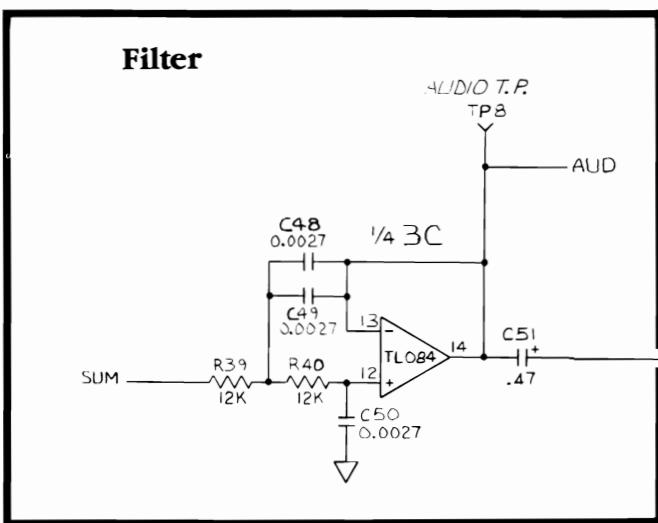
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Music and Sound Effects Generators





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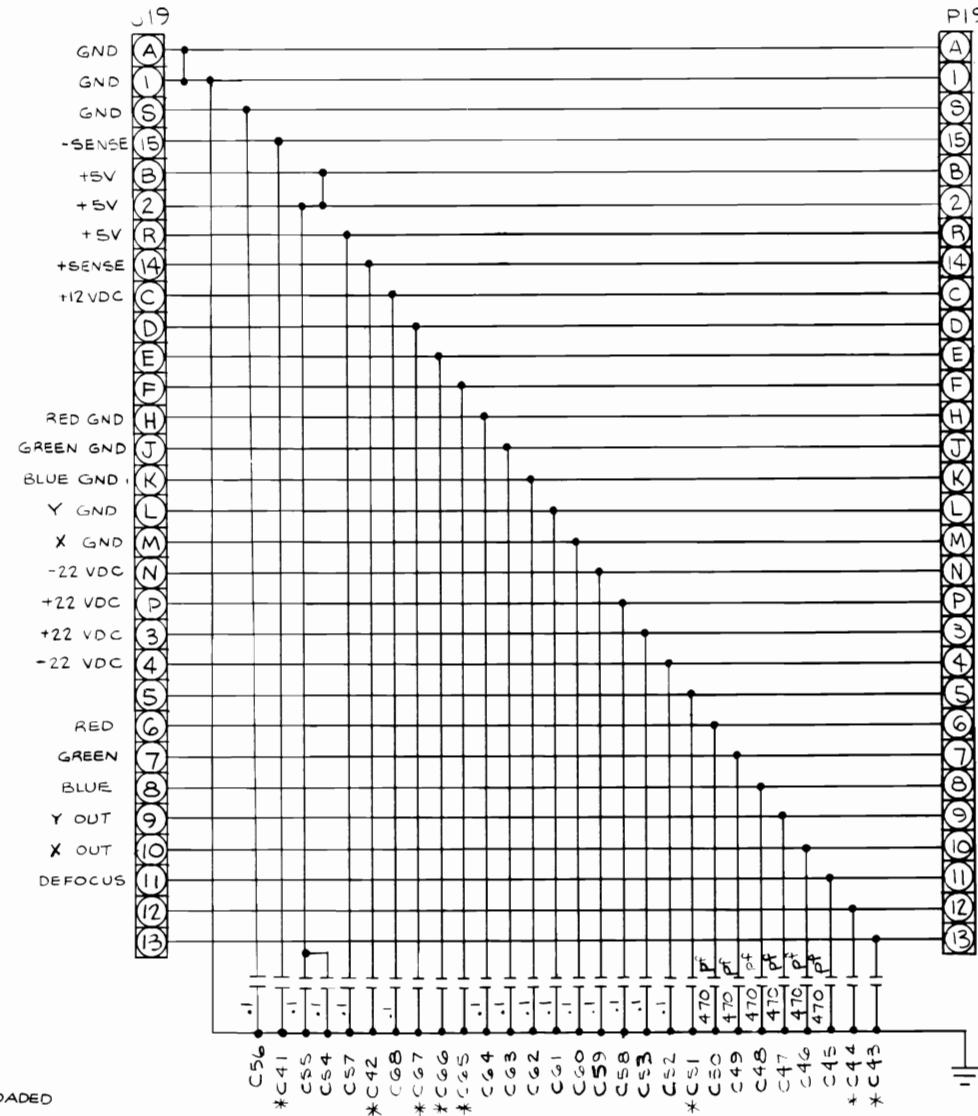
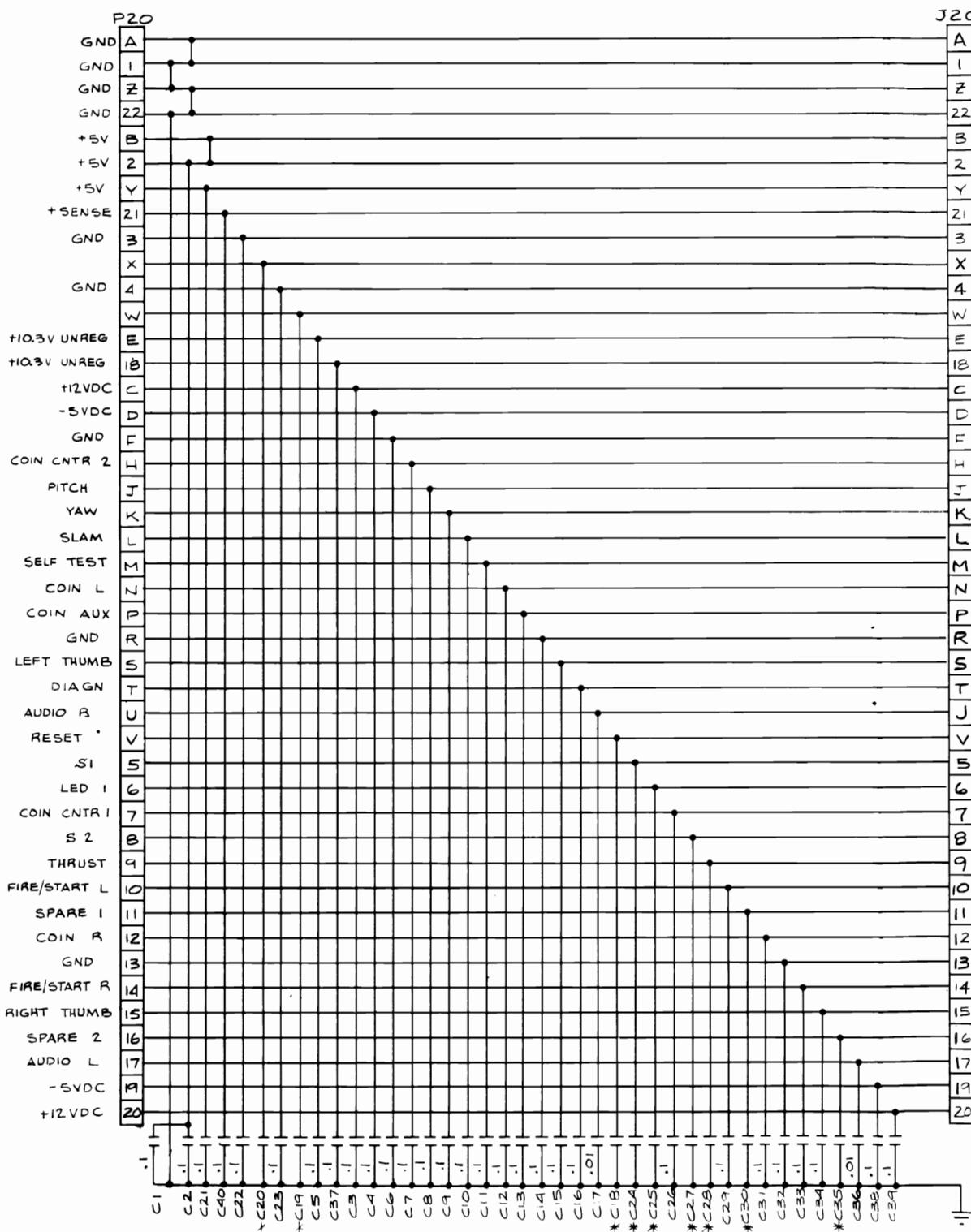
STAR WARS Sound PCB



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NOTE:
1.* = NOT LOADED

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STAR WARS EMI Shield PCB



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STAR WARS Interconnect PCB



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